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SEMINARS/WORKSHOPS ORGANIZED DURING A.Y.:2018-2019



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

ISO 9001:2015 Certified

(Approved by AICTE, New Delhi, Affiliated to JNTUGV, Vizianagaram)

Office: +9109705576693, email: principal_svcet@yahoo.com,

Website: www.svcet.net



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

List of Seminars/ workshops

A.Y.:2018-2019

S.NO.	Name of the Programme	Date	No. of Participants	Details of the Resource Person
1	A One Day Seminar On Intellectual Property Rights With Focus On Copy Rights And Patents	13.06.2018	37	Mr. K.Prema Kumar, Associate Professor, Sri Sivani College Of Engineering, Chilakapalem, Srikakulam: prema.tlc@gmail.com
2	A One Day Seminar On Useful Tips On Meeting Regulatory Requirements For Research	13.08.2018	30	Dr.D.Vijay Kumar, Professor & HoD.EEE, AITAM, Tekkali
3	A One Day Seminar On Social And Economic Implications Of Intellectual Property Rights	20.08.2018	27	A. Apparao , Associate Professor, SVCET, Etcherla, Srikakulam:
4	A One-Day Workshop On Empowering Entrepreneurial Endeavors Fostering Innovation Through Idea	14.09.2018	42	Dr K.Satyam, Professor, MBA Department, SISTAM, Srikakulam
5	A One-Day Workshop On Titled Risk Management In Entrepreneurship A Path To Success	04.10.2018	45	CH. Keerthana, Asst.professor, Dept. Of MBA, SSCE, Srikakulam
6	A One Day Seminar On The Challenges Of Intellectual Property Rights And Protection Of Human Rights With Special Reference To Sustainable Development	19.12.2018	35	Dr. J. Bala Bhaskara Rao, Professor, SSCE, Srikakulam: drjbbrao@gmail.com
7	A One Day Seminar On Potential Challenges Faced In Implementing Intellectual Property Rights In India	23.01.2019	40	Dasari. Tirumala Rao, Associate Professor, SVCET, Etcherla, Srikakulam.
8	A One Day Seminar On Research And New Trends In Electrical Engineering	06.03.2019	30	Dr.P.Devendra, Professor in Department of EEE GVPCEW, Visakhapatnam
9	A One Day Seminar On Intellectual Property Rights: Changing Dimensions Of Patent Laws In India	10.04.2019	47	Mr. V.K.Chkravathi, Associate Professor, Department of Civil Engineering, SVCET, Etcherla, Srikakulam


IQAC


Principal

PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)



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CIRCULAR

Date 06-06-2018

This is to inform to all the students of B-Tech that we are going to conduct **A One Day Seminar on Intellectual Property Rights with Focus on Copy Rights and Patents** on 13/06/2018 [10A.M to 4P.M]. All the students have to report at the seminar hall sharp at 9:30am with the proper dress code and with ID cards. Attendance is mandatory.

Resource person: Mr. K.Prema Kumar, Associate Professor, Sri Sivani College Of Engineering, Chilakapalem, Srikakulam

All the students have to attend the seminar in the digital hall of the BS&H department at 9:30A.M without fail.












This program is aimed to: The participants get knowledge on, IPR overview, types of IPR and their importance, patents, trademark and copyrights.


Program coordinator

Submitted to: Honorable Management for Favor of Information

Copy To: Honorable Management for Favor of Information:


Principal
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Department	IQAC	CE	EEE	ME	ECE	CSE	BS&H	MBA	OFFICE	NB	FILE
Signature											

About the Institution:

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

About the Department:

Electronics and Communication Engineering departments typically offer undergraduate, graduate, and Post Graduate programs. Students in these programs learn foundational principles through coursework, engage in hands-on projects and research, and often have opportunities for internships or co-op experiences in industry. The department of ECE was established in 2008 with aim of importing high quality technical education in the area with an intake of 120. The department of ECE offers many courses that can be chosen as electives by the students of the other departments. The curriculum has been designed to take the college to levels on par with advanced institutes.

CHIEF PATRON:

Dr. B. SRI RAMA MURTHY,
SECRETARY,
VIKAS EDUCATIONAL SOCIETY

PATRON:

Dr. M. GOVINDHA RAJU
Principal, SVCET

CO-ORDINATOR:

Mr. K. SRINIVAS RAO
Asst. Professor, ECE Dept
SVCET
CELL: 9885947733
EMAIL: ksrinu.19@gmail.com

RESOURCE PERSON:

Mr. K. Prema Kumar
Associate Professor
Department of Mechanical Engineering
SSCE, Srikakulam

A ONE DAY SEMINAR ON INTELLECTUAL PROPERTY RIGHTS WITH FOCUS ON COPY RIGHTS AND PATENTS

13-06-2018



Organized by

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING**
**SRI VENKATESWARA COLLEGE OF
ENGINEERING AND TECHNOLOGY**
(Under the Management of Vikas Educational
Society, Srikakulam)

Accredited by NAAC with 'A' Grade
(Approved by AICTE, New Delhi & Affiliated to
JNTUK, Kakinada; G.O.Ms.No.101, Dt. 09-07-08)
NH-5, ETCHERLA-532410, SRIKAKULAM (Dt)
ANDHRA PRADESH
Website: www.svcet.info,

Email: principal_svcet@yahoo.com

Department Vision:

Emerge as a center of pre-Enhance in Electronics and communication Engineering Education, Research and Services.

Department Mission:

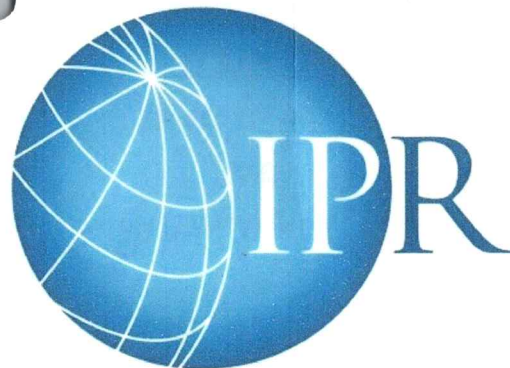
Our mission is to Produce graduates with scientific and technical Knowledge and excellent electronic and communication engineering skills for professional practice employment, advance study and research. To introduced Professional and ethical values related to industry, Society and environment. To interact with industries, to meet their requirements through employable graduates.

Objective:

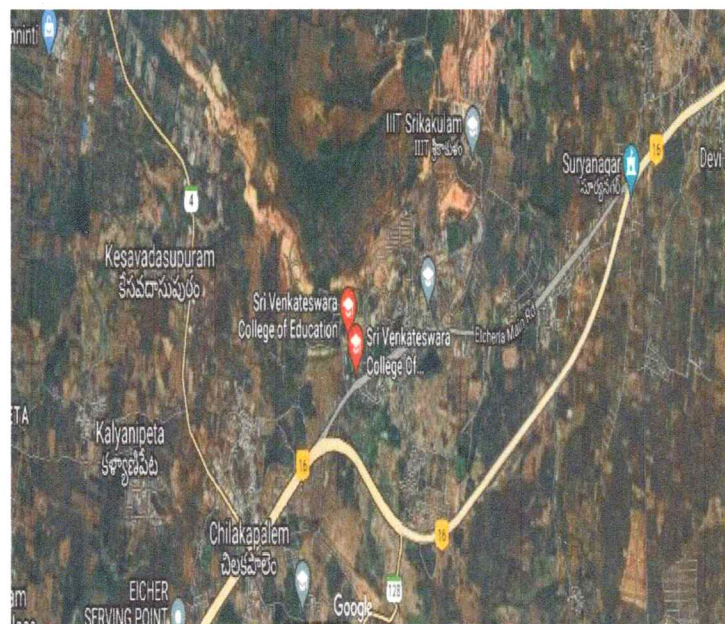
The objective of the program is to provide participants with a comprehensive understanding of intellectual property rights, specifically focusing on copyrights and patents.

Expected Outcomes:

Participants should be able to identify what constitutes intellectual property within their own work or business context, whether it be literary works, artistic creations, software, inventions, or designs.



Route Map:



Sri Venkateswara College of Engineering and Technology
(Under the Management of Vikas Educational Society, Srikakulam)

REGISTRATION FORM

“A ONE DAY SEMINAR ON INTELLECTUAL PROPERTY RIGHTS WITH FOCUS ON COPY RIGHTS AND PATENTS”

13-06-2018

Participant's name :

Gender: Male/Female :

Year & Sem :

Branch :

Organization :

Address for Communication :

Mobile Number :

Email ID :

Date: **Signature of the Participant**

Signature of the HOD



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A ONE DAY SEMINAR ON INTELLECTUAL PROPERTY RIGHTS WITH FOCUS ON COPY RIGHTS AND PATENTS

List of Enrolled Students with Signature

Date: 13-06-2018

S-NO	NAME OF THE PARTICIPANT	REGD-No	BRANCH	SINGNATURE
1	ARIKA KALYAN	15MT1A0403	ECE-IV	A. Kalyani
2	BHEERANGI RATNALAMMA	15MT1A0405	ECE-IV	B. Ratnalamma
3	CHEKKA HARIKA	15MT1A0407	ECE-IV	C. Harika
4	GANTA KRANTHI KUMAR	15MT1A0408	ECE-IV	G. Kranthi Kumar
5	GARA ESWARI	15MT1A0409	ECE-IV	G. Eswari
6	GUNANA RANI	15MT1A0410	ECE-IV	G. Rani
7	KAGANA PRADEEP	15MT1A0411	ECE-IV	K. Pradeep
8	KILLAM SHETTY PRAVEEN	15MT1A0412	ECE-IV	K. Praveen
9	KUNU PRAVEENA	15MT1A0414	ECE-IV	K. PRAVEEN
10	LINGALA NANDINI	15MT1A0415	ECE-IV	L. Nandini
11	MALLA KUMARA SWAMY	15MT1A0417	ECE-IV	M. Kumara Swamy
12	MEENAKA MAHESWARI	15MT1A0418	ECE-IV	M. Maheswari
13	NIMMAKA LAILA	15MT1A0419	ECE-IV	N. Laila
14	NIMMAKA SANDYARANI	15MT1A0420	ECE-IV	N. Sandhyarani
15	PALASA ANITHA	15MT1A0421	ECE-IV	P. Anitha
16	PALLA TEJASWANI YADAV	15MT1A0422	ECE-IV	P. Tejaswani Yadav
17	PODELA SANDHYA	15MT1A0423	ECE-IV	P. Sandhya
18	PUVVALA MALLIKA	15MT1A0424	ECE-IV	P. Mallika
19	PUVVALA SANTHOSHIKUMARI	15MT1A0425	ECE-IV	P. Santhoshi Kumari
20	RATNALA NAVYA	15MT1A0426	ECE-IV	R. Navya
21	SANTHO SURESH	15MT1A0427	ECE-IV	S. Suresh
22	SEEPANA GNANESWARI	15MT1A0429	ECE-IV	S. Gnaneswari
23	SIMHADRI SIREESHA	15MT1A0430	ECE-IV	S. Sireesha
24	TADANGI VINODHKUMAR	15MT1A0431	ECE-IV	T. Vinod Kumar
25	THERLAPU MEENA	15MT1A0432	ECE-IV	T. Meena
26	TOYAKA SUKANYA	15MT1A0433	ECE-IV	T. Sukanya
27	VANTARI GANGA BHAVANI	15MT1A0434	ECE-IV	V. Bhavani
28	YAJJALA RAVI KUMAR	15MT1A0435	ECE-IV	Y. Ravi Kumar
29	TELUKALA ASISH KUMAR	15MT1A0437	ECE-IV	T. Asish Kumar
30	A RAJESH VARMA	16MT5A0401	ECE-IV	A. Rajesh Varma
31	ABOTHULA UMA MAHESWARA RAO	16MT5A0402	ECE-IV	A. Umamaheswara Rao
32	BANTUPALLI MALLESWARA RAO	16MT5A0403	ECE-IV	B. Malleswara Rao
33	BARATAM PRAVALLIKA	16MT5A0404	ECE-IV	B. Pravallika

34	BASAVA SANDEEP KUMAR	16MT5A0406	ECE-IV	B. Sandeep Kumar
35	GARA AJIT KUMAR	16MT5A0407	ECE-IV	G. Ajit Kumar
36	KALAVALA NAGENDRA	16MT5A0408	ECE-IV	K. Nagendra
37	KARUKOLA MEGHANA	16MT5A0409	ECE-IV	K. meghana


IQAC


PROGRAM CO-ORDINATOR



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Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date 14-06-2018

It is to inform that **A One Day Seminar on Intellectual Property Rights with Focus on Copy Rights and Patents** has been conducted during 13th June 2018 in the Digital hall of BS&H department.

RESOURCE PERSON:

Mr. K. Prema Kumar

Assoc. Professor

Sri Sivani College Of Engineering

Chilakapalem, Srikakulam

CO-ORDINATOR:

Mr. K. Srinivas Rao

Assistant Professor

ECE dept,

SVCET

Number of Students Participated: 37

OBJECTIVES OF THE PROGRAM

The objective of the Intellectual Property Rights (IPR), with a concentrated focus on copyrights and patents, is to provide participants with a comprehensive understanding of the legal, economic, and societal implications of intellectual property protection in the digital age. Through insightful discussions, expert presentations, and interactive sessions, the seminar aims to explore the evolving landscape of copyrights and patents, including recent legislative developments, emerging challenges, and best practices. By fostering interdisciplinary dialogue and knowledge exchange among legal experts, industry professionals, policymakers, and academics, the seminar

seeks to empower participants with the tools, strategies, and insights needed to navigate the complexities of intellectual property management, protect their creative and innovative endeavors, and contribute to the advancement of a balanced and inclusive intellectual property regime that fosters innovation, creativity, and sustainable development.

OUTCOMES OF THE PROGRAM

Attendees should acquire a comprehensive understanding of copyright law, including what types of works are eligible for copyright protection, the rights granted to copyright holders, the duration of copyright protection, and the concept of fair use.



Signature of the program coordinator



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Web : www.svcet.info, Email : principal_svcet@yahoo.com

Date: 10/08/2018

CIRCULAR

This is to inform to all the students of B.Tech. that the Dept. of EEE is going to conduct A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH On 13th Aug 2018 [10.00 am to 04.00pm].

Resource Person : Dr.D.Vijay Kumar, Professor & HoD/EEE, AITAM, Tekkali

Date : 13th -Aug-2018

All the students have to report at the seminar hall sharp at 09:45am with the proper dress code and with ID cards. Attendance is mandatory.












Venue: Seminar Hall, Block-A


PROGRAM COORDINATOR


PRINCIPAL

PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Submitted to: Honorable Management for favor of information

Department	IQAC	CIVIL	EEE	MECH	ECE	CSE	BS&H	MBA	OFFICE	Notice Board	File
Signature											

ABOUT THE PROGRAM

Objective:

The objective of the program is to bring awareness in the young technical minds to understand themselves knowledge and confident in research. This in turn, will improve our research quality, enhance our reputation, and mitigate potential risks associated with non-compliance. Its provide practical guidance and insights into navigating the complex landscape of regulatory requirements in research.

Expected Outcomes:

All The Students Who Have Participated In A One Day Seminar On “ **A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH**” Have Felt Free And Realized The Importance Of Research And Very Much Enthusiastic To Get Them Absorbed In Research After Completion Of Their Studies with valuable insights and practical knowledge. Researchers will be better prepared to meet regulatory requirements effectively, thereby enhancing their research practices and contributing to the overall success and credibility of their research endeavors.

****Spot Registration is available****

CHIEF PATRON

Dr.B.Sri Rama Murthy
Secretary,SVC

PATRON

Dr M.Govinda Raju
Principal,SVCET

CONVENER

A.Apparao
HOD, EEE Dept
Email id:3esvcet@gmail.com

CO-ORDINATOR

J.Hareesh
Assistant Professor, EEE Dept,SVCET

A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH

13th-AUG-2018



Organized by
**DEPARTMENT OF
ELECTRICAL & ELECTRONICS
ENGINEERING**

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ENGINEERING & TECHNOLOGY**

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AICTE)

**AndhraPradesh- 532410
Phone:+91-9440220356**

www.svcet.info

ABOUT THE INSTITUTION

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

ABOUT THE DEPARTMENT

Electrical and Electronics Engineering is a profession that uses science, technology and problem solving skills to design construct and maintain products. It is a challenging but exciting and rewarding area to study. The department of EEE was established in 2008 with aim of importing high quality technical education in the area of Electrical and Electronics Engineering with an intake of 60. The department offers post graduate program in the specialization of Power Electronics and control systems. The department of EEE offers many courses that can be chosen as electives by the students of the other departments. The curriculum has been designed to take the college to levels on par with advanced institutes.

The EEE Department has well qualified and experienced faculty members supported by trainers. The department adopts the right blend of practical approaches in training the students and placements.

DEPARTMENT VISION

To be a globally recognized center of excellence in Electrical and Electronic Engineering, pioneering innovations that shape the future of technology and society.

DEPARTMENT MISSION

Our mission is to provide transformative education, conduct cutting-edge research, and foster innovation in Electrical and Electronic Engineering to address global challenges and improve quality of life.

RESOURCE PERSON

Dr.D.Vijay Kumar

Professor & HoD, Dept.of EEE
Aditya Institute of Technology and
Management,Tekkali.

REGISTRATION FORM

**"A ONE DAY SEMINAR ON USEFUL
TIPS ON MEETING REGULATORY
REQUIREMENTS FOR RESEARCH "**

13th-Aug-2018

Name: _____

ROLL NO: _____

Institution: _____

Mobile No: _____

Email: _____

Date: _____ Signature of the Participant

SPONSORSHIP CERTIFICATE

Mr./Ms.____ is an Student of this institution and he/she is sponsored to attend the Seminar on
**"A ONE DAY SEMINAR ON USEFUL
TIPS ON MEETING REGULATORY
REQUIREMENTS FOR RESEARCH "**

**Signature of the HOD
With Seal**



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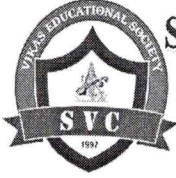
Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH

List of Participants

Date: 13/08/18

S. No	Regd. No	Name of the Student	Year & Sem	Signature
1	16MT1A0201	BADITHA HARISH	EEE-III	B. Harish
2	16MT1A0205	B PRAKASH	EEE-III	B. Prakash
3	16MT1A0206	C UMAVATHI	EEE-III	C. Umavathi
4	16MT1A0208	E RAJASEKHAR	EEE-III	E. Rajasekhar
5	16MT1A0209	G TEJESWARA RAO	EEE-III	G. Tejaswara Rao
6	16MT1A0210	GUNTU RAHUL	EEE-III	G. Rahul
7	16MT1A0211	JANNI PUJA	EEE-III	J. Puja
8	16MT1A0212	JARUGULLA RAJANI	EEE-III	J. Rajani
9	16MT1A0214	KANTA PRIYANKA	EEE-III	K. Priyanka
10	16MT1A0215	KINJARAPU SAILAJA	EEE-III	K. Sailaja
11	16MT1A0216	KOTTAPALLI ANUSHA	EEE-III	K. Anusha
12	16MT1A0218	L KUSUMAKUMARI	EEE-III	L. Kusumakumari
13	16MT1A0219	MEESALA RAMBABU	EEE-III	M. Rambabu
14	16MT1A0223	PEDADA UPENDRA	EEE-III	P. Upendra
15	16MT1A0224	ROKKAM LINGA RAJU	EEE-III	R. Lingaraju
16	16MT1A0226	SIRLI HEMA LATHA	EEE-III	S. Latha



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17	16MT1A0227	TOMPALA SANDHYA	EEE-III	T. Sandhya
18	16MT1A0228	Y RAJENDRA PRASAD	EEE-III	Y. Rajendra Prasad
19	17MT5A0201	ALIKANA USHA	EEE-III	A. USHA
20	17MT5A0202	B HEMANTHA RAO	EEE-III	B. Hemantha Rao
21	17MT5A0203	B DURGA PRASAD	EEE-III	B. Durga Prasad
22	17MT5A0204	BATTINI PRAVEEN	EEE-III	B. Praveen
23	17MT5A0205	BINDUMADAVI GOLLU	EEE-III	B. Gollu
24	17MT5A0206	CHINNI DURGAPRASAD	EEE-III	C. Durga Prasad
25	17MT5A0207	CHINTADA RAMYA	EEE-III	C. Ramya
26	17MT5A0208	DANDUPATI RAJESH	EEE-III	D. Rajesh
27	17MT5A0209	D NIRANJAN KUMAR	EEE-III	D. Niranjan
28	17MT5A0210	GUNDU RAJASEKHAR	EEE-III	G. Rajasekhara
29	17MT5A0212	JAMMA MEENA	EEE-III	J. Meena
30	17MT5A0213	K SAMPATHKUMAR	EEE-III	K. Sampathkumar


IQAC


PROGRAMME COORDINATOR



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Web : www.svcet.info, Email : principal_svcet@yahoo.com

Date: 14/08/18

PROGRAM REPORT

A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH has been conducted on **13th -Aug-2018** [10am to 4pm] in the Seminar hall of EEE department.

The following member is invited as a Resource person for the seminar.

Name: Dr. D.Vijay Kumar, Professor & HoD/EEE, Aditya Institute of Technology and Management (AITAM), Tekkali.

Mr.J.Hareesh, Asst.Professor, Dept.of EEE is the coordinator of the program.

Number of Students attended: 30

The Resource person has expressed categorically the importance, day to day usage in life regarding the subject. This seminar marks a significant step towards enhancing our research practices and ensuring compliance with regulatory requirements. The seminar was well-received by all participants. All the participants felt highly impressed and motivated and realized the importance of the Seminar.

OBJECTIVES OF THE PROGRAM

The objective of the program is to bring awareness in the young technical minds to understand themselves knowledge and confident in research. The objective of the program is to bring awareness in the young technical minds to understand themselves knowledge and confident in research. This in turn, will improve our research quality, enhance our reputation.



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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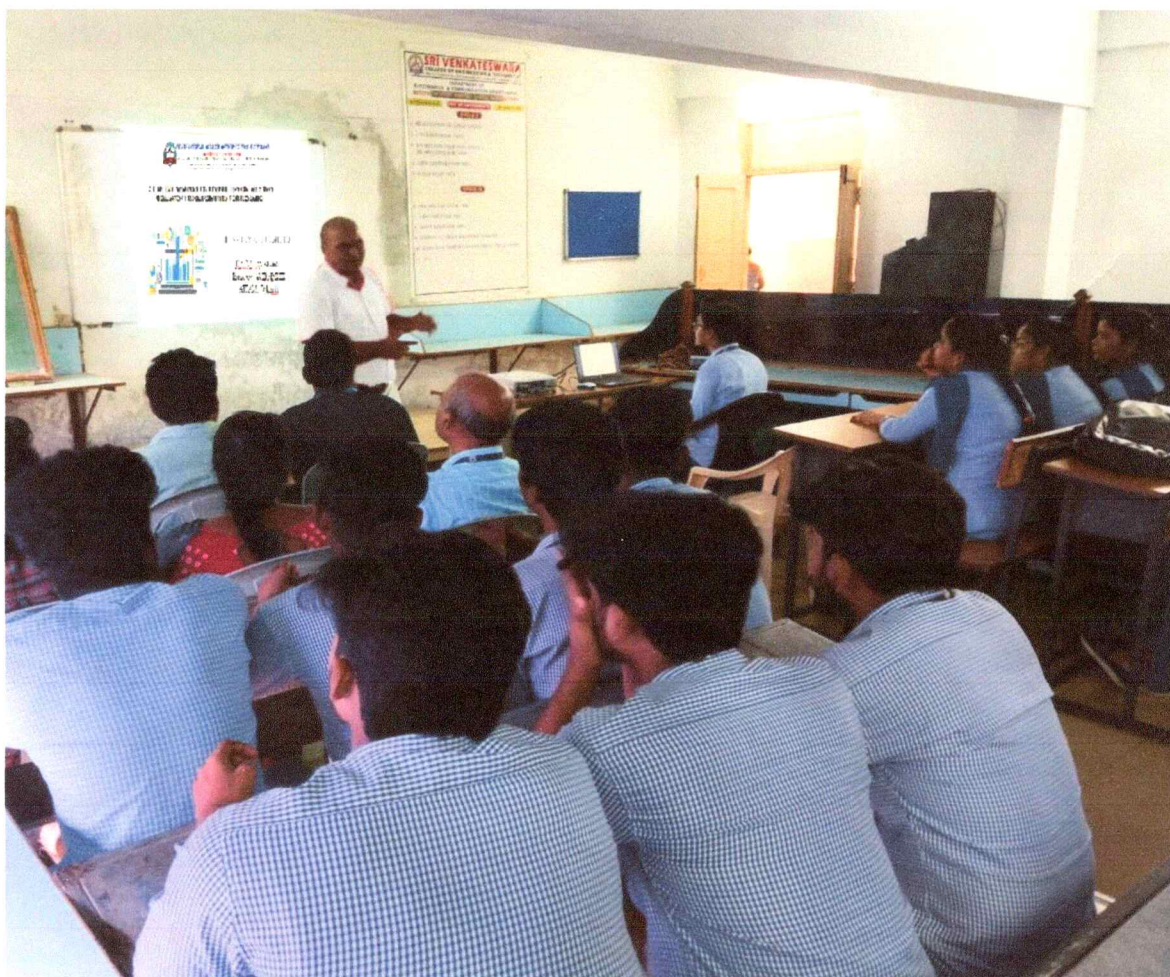
(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

OUTCOMES OF THE PROGRAM

All the students who have participated in “A ONE DAY SEMINAR ON USEFUL TIPS ON MEETING REGULATORY REQUIREMENTS FOR RESEARCH” have felt free and realized the importance of research and very much enthusiastic to get them absorbed in research after completion of their studies. The students not only gained valuable knowledge and skills but also developed a newfound enthusiasm for pursuing research in their future careers.



Signature of the coordinator



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CIRCULAR

Date 07-08-2018

This is to inform to all the students of B-Tech that we are going to conduct **A One Day Seminar On Social And Economic Implications Of Intellectual Property Rights** on **20/08/2018** [10A.M to 4P.M] All the students have to report at the seminar hall sharp at 9:30A.M with the proper dress code and with ID cards. Attendance is mandatory.

Resource person: Mr. A. Apparao, Associate Professor, SVCET, Etcherla, Srikakulam

All the students have to attend the seminar of the BS&H department at 9:30A.M

This program is aimed to:












- Importance and scope of IPR in Academic Researches.
- Types of innovations patentable & how to test your patentability of your innovations.
- Procedure of patent writing, patent filing and the process of granting patent.


Program coordinator

Submitted to: Honorable Management for Favor of Information


Principal
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Copy To:

Department	IQAC	CE	EEE	ME	ECE	CSE	BS&H	MBA	OFFICE	NB	FILE
Signature											

About the Institution:

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

About the Department:

Electronics and Communication Engineering departments typically offer undergraduate, graduate, and Post Graduate programs. Students in these programs learn foundational principles through coursework, engage in hands-on projects and research, and often have opportunities for internships or co-op experiences in industry. The department of ECE was established in 2008 with aim of importing high quality technical education in the area with an intake of 60. The department of ECE offers many courses that can be chosen as electives by the students of the other departments. The curriculum has been designed to take the college to levels on par with advanced institutes.

CHIEF PATRON:

Dr. B. SRI RAMA MURTHY,
SECRETARY,
VIKAS EDUCATIONAL SOCIETY

PATRON:

Dr. M. GOVINDHA RAJU
PRINCIPAL, SVCET

CO-ORDINATOR:

Mr. K. SRINIVAS RAO
Asst. Professor, ECE Dept
CELL: 9885947733
EMAIL: ksrinu.19@gmail.com

RESOURCE PERSON:

Mr. A. APPARAO
Associate Professor, EEE Dept
SVCET

A ONE DAY SEMINAR ON SOCIAL AND ECONOMIC IMPLICATIONS OF INTELLECTUAL PROPERTY RIGHTS

20-08-2018



Organized by

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING**
**SRI VENKATESWARA COLLEGE OF
ENGINEERING AND TECHNOLOGY**
(Under the Management of Vikas Educational
Society, Srikakulam)

Accredited by NAAC with 'A' Grade
(Approved by AICTE, New Delhi & Affiliated to
JNTUK, Kakinada; G.O.Ms.No.101, Dt. 09-07-08)
NH-5, ETCHERLA-532410, SRIKAKULAM (Dt)

ANDHRA PRADESH

Website: www.svcet.info,

Email: principal_svcet@yahoo.com

Department Vision:

Emerge as a center of pre-Enhance in Electronics and communication Engineering Education, Research and Services.

Department Mission:

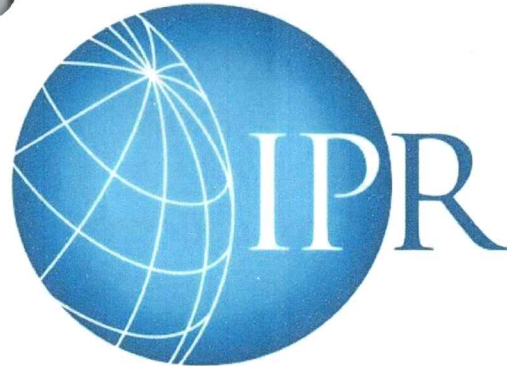
Our mission is to Produce graduates with scientific and technical Knowledge and excellent electronic and communication engineering skills for professional practice employment, advance study and research. To introduced Professional and ethical values related to industry, Society and environment. To interact with industries, to meet their requirements through employable graduates.

Objective:

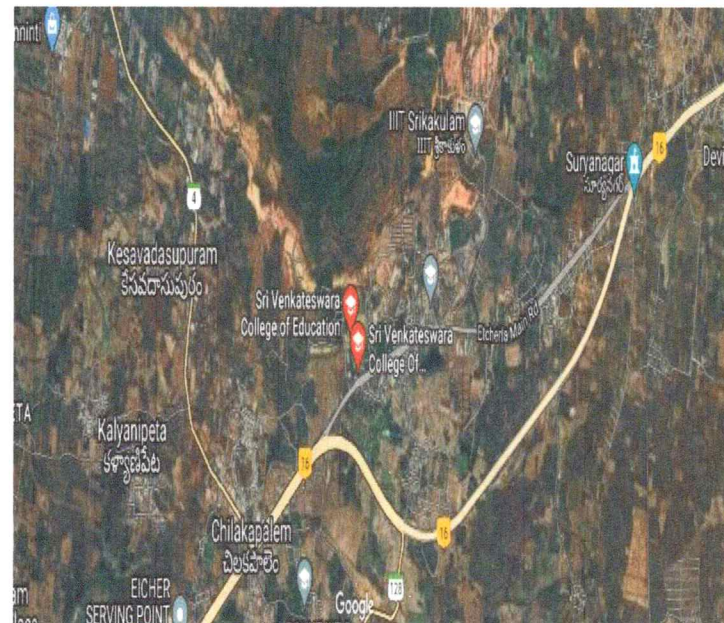
The objective of the program is o provide participants with a comprehensive understanding of the social and economic significance of intellectual property rights, including their role in fostering innovation, creativity, and economic growth.

Expected Outcomes:

Participants should be inspired to take proactive measures within their spheres of influence to promote a more inclusive, equitable, and sustainable intellectual property ecosystem that serves the interests of both creators and society as a whole.



Route Map:



Sri Venkateswara College of Engineering and Technology
(Under the Management of Vikas Educational Society, Srikakulam)

REGISTRATION FORM

“A ONE DAY SEMINAR ON SOCIAL AND ECONOMIC IMPLICATIONS OF INTELLECTUAL PROPERTY RIGHTS”

20-08-2018

Participant's name :

Gender: Male/Female :

Year & Sem :

Branch :

Organization :

Address for Communication :

Mobile Number :

Email ID :

Date: **Signature of the Participant**

Signature of the HOD



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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON SOCIAL AND ECONOMIC IMPLICATIONS OF INTELLECTUAL PROPERTY RIGHTS

List of Enrolled Students with Signature

Date: 20-08-2018

S-NO	NAME OF THE PARTICIPANT	REDG-NO	BRANCH	SINGNATURE
1	ARANGI URMILA	15MT1A0501	CSE-IV	A. Urmila
2	ARIKA INDU	15MT1A0502	CSE-IV	A. Indu
3	DHARMANA PAVANI	15MT1A0507	CSE-IV	D. Pavani
4	DOLA INDIRA	15MT1A0508	CSE-IV	D. Indira
5	GANDI MOUNIKA	15MT1A0509	CSE-IV	Mounika G
6	GONNABHAKTULA VASUDHA	15MT1A0510	CSE-IV	G. Vasudha
7	GUMPU LAKSHMI PARVATHI	15MT1A0512	CSE-IV	G. Lakshmi Parvathi
8	JEMINIVALASA GOWTHAMI	15MT1A0515	CSE-IV	G. PARVATHI
9	KADAMBALA DIVYA	15MT1A0516	CSE-IV	K. Divya
10	KILLAKA VANAJAKSHI	15MT1A0517	CSE-IV	K. Vanajakshi
11	KONDALA SAI KUMAR	15MT1A0518	CSE-IV	K. Sai Kumar
12	KONNA MANASA	15MT1A0519	CSE-IV	K. Manasa
13	KUPPILI GIRIJARANI	15MT1A0521	CSE-IV	Girijarani
14	LAKE PADMA	15MT1A0522	CSE-IV	L. Padma
15	LINGALA PRASANNA	15MT1A0523	CSE-IV	L. Prakash
16	MALLA RAJESH	15MT1A0525	CSE-IV	M. Rajesh
17	MAMIDI SANDHYA RANI	15MT1A0526	CSE-IV	M. Sandhya Rani
18	NAGIREDDI ASWINI	15MT1A0528	CSE-IV	N. Aswini
19	PODUGU KRUPA RANI	15MT1A0530	CSE-IV	P. Krupa Rani
20	PUCCHA BHAVANI	15MT1A0531	CSE-IV	P. Bhavani
21	SIRLI RAJYA LAKSHMI	15MT1A0533	CSE-IV	S. Rajya Lakshmi
22	YADLA PRASAD	15MT1A0534	CSE-IV	Y. Prasad
23	YEDDU SRAVANI	15MT1A0535	CSE-IV	Y. Sravani
24	BEVARA VIJAYA LAKSHMI	15MT1A0537	CSE-IV	B. Vijaya Lakshmi
25	PANGA SARAT KUMAR	15MT1A0538	CSE-IV	P. Sarat Kumar
26	T MANOJ KUMAR	15MT1A0539	CSE-IV	T. Manoj Kumar
27	ALAPANA KESAVI KUMARI	16MT5A0502	CSE-IV	A. Kesavi Kumari

IQAC

PROGRAM CO-ORDINTOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date 22-08-2018

It is to inform that **A One Day Seminar on Social and Economic Implications of Intellectual Property Rights** has been conducted during 13th June 2018 in the Digital hall of BS&H department.

RESOURCE PERSON:

Mr. A. Apparao
Associate Professor
SVCET
Etcherla, Srikakulam

CO-ORDINATOR:

Mr. K. Srinivas Rao
Assistant Professor
ECE department
SVCET

Number of Students Participated: 27

OBJECTIVES OF THE PROGRAM

In today's rapidly evolving global economy, intellectual property rights (IPRs) play a crucial role in shaping social and economic landscapes. This one-day seminar aims to delve deep into the multifaceted implications of IPRs, analyzing their effects on innovation, entrepreneurship, social equity, and economic development.



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

OUTCOMES OF THE PROGRAM

The outcome of a one-day seminar on the social and economic implications of intellectual property rights (IPRs) can be multifaceted and impactful. Attendees gain a deeper understanding of the importance of intellectual property rights in driving innovation, fostering entrepreneurship, and shaping economic development.



Signature of the program coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

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CIRCULAR

Date: 07/09/2018

This is to inform to all the students that the EDC Cell is going to conduct A **One-day Workshop on Empowering Entrepreneurial Endeavors Fostering innovation through idea generation.**

The workshop aimed to make students aware of Entrepreneurship through idea generation, encourage and motivate them develop idea generation is one of the main factors that lead to its success.

Coordinator : MR. L. PRASADA RAO, Asst Prof, Dept of MBA ,SVCET.

Resource Person: DR. K. SATYAM, Associate Professor, Dept Of MBA, SISTAM Engineering College, Srikakulam

Date : 14th -SEP-2018

All the students must attend the seminar in the Seminar hall at 9:30am with the proper dress code & ID cards.














Programme Coordinator


Principal

PRINCIPAL

Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Copy submitted to: Honourable Management for favour of information

Department	IQAC	CE	EEE	ME	ECE	CSE	MBA	BS&H	EDC	NOTICE BOARD	OFFICE	FILE
Signature												

PROGRAMME SCHEDULE:

09:30 a.m. to 10:00 a.m.	: Registration
10:00 a.m. to 11:00 a.m.	: Inauguration
11:00 a.m. to 11:15 a.m.	: Tea Break
11:15 a.m. to 01:00 p.m.	: Lecture – I
01:00 p.m. to 02:00 p.m.	: Lunch Break
02:00 p.m. to 03:30 p.m.	: Lecture – II
03:30 p.m. to 03:45 p.m.	: Tea Break
03:45 p.m. to 04:30 p.m.	: Valedictory

Conveners:

Smt.V.SANTHI, Assistant Professor, HOD-MBA

Mr. L.PRASADA RAO, Assistant Professor, Dept. of MBA.

ROUTE MAP:



ORGANIZING COMMITTEE

Chief Patron:

Dr.B.SRI RAMA MURTHY
Secretary,VES

Patrons:

Dr. M.GOVINDA RAJU, Principal, SVCET

Co-Patron

Sri I. Kishore, Director,VES,ISVC
Dr.B .S.RAJA,Director,VES
Sri B .Raghu Karthik, Director,
Sri B.Krishna Vamsi, Director,

Convener:

Smt. V. Santhi, Assistant professor Dept of
HOD

Coordinator:

Mr. L Prasada Rao, Asst Professor

Faculty Coordinators:

Dr P.RAVI KUMAR,Asst.professor,M.B.A
Mr.M.RAJA MOHAN,Asst.professor,M.B.A
Mr.P.VENKATSWARA RAO, Asst.professor,M.B.A
Mr.S.SRIKANTH,Asst.professor,M.B.A
Mr.A.NAVEENA,Asst.professor,M.B.A
Mr.K.CHANDRA SEKHAR,Asst.professor,M.B.A
Mr.K. DURAGARAO, Asst Professor, MBA
Mr. S.BHASKAR RAO, Asst. Prof. C.S.E
Mr.B.MURALI, Asst. Prof. E.E.E
Mr.K ESWARA RAO, Asst. Prof. Mech
Mr.N.SIMHAHALAM, Asst. Prof. Civil
Mr.V.JEEVAN KUMAR, Asst. Prof. E.C.E
Mr. B.PARANDHAM,Asst,pfof BS&H

A ONE DAY WORKSHOP

ON

EMPOWERMENT ENTREPRENEURIAL ENDEAVORS FOSTERING INNOVATION THROUGH IDEA GENERATION

14th September,2018



Organized by
ENTREPRENEURSHIP DEVELOPMENT CELL

SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY
Accredited by NAAC "A" GRADE
(Affiliated by JNTUK Kakinada & Approved by AICTE)
ETHERLA, SRIKAKULAM.
ANDHRA PRADESH. 532 410

ABOUT THE COLLEGE:

Sri Venkateswara College of Engineering & Technology, Etcherla, Srikakulam (Dist.) was established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. SVCET is accredited by NAAC in 2016. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow end owed with highly qualified and seasoned faculty on its rolls. Sri Venkateswara College of Engineering & Technology is committed to "Impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values. The Institute is a recipient of Best Green Campus award for 2017 by Govt. of A.P. in 2018.

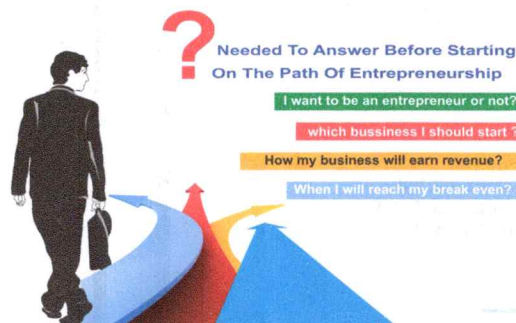
ABOUT EDC:

Entrepreneurship Development Cell (EDC) is being promoted in SVCET to develop institutional mechanism to create entrepreneurial culture and to foster entrepreneurship for generation of wealth and employment. To guide and assist prospective entrepreneurs on various aspects such as preparing project reports, obtaining project approvals, loans and facilities from agencies of support system, information on technologies, To arrange interaction with entrepreneurs and create a mentorship scheme for student entrepreneurs.

Cell is a core area in an Engineering College which complements the efforts of the members of faculty by organizing value-added programs and providing placement opportunities to students. The cell continuously coordinates with various industries and organizations for placement of the students.

ABOUT THE WORKSHOP:

A workshop on 'Empowerment Entrepreneurial Endeavors Fostering Innovation Through Idea Generation' is a beneficial objective in the field of Entrepreneurship. It is a practical tool for helping participants to develop Personal Entrepreneurial Competencies (PECs). Entrepreneurship is a dynamic process of vision change and creation. It requires an application of energy and passion towards the creation and implementation of new ideas and creative solutions. Essential ingredients include the willingness to take calculated risks – in terms of time, equity, or career; the ability to formulate an effective venture team; the creative skill to marshal needed resources; and fundamental skill of building solid business plan.



RESOURCE PERSON AND TOPICS

Resource Persons:

Dr. K.SATYAM,
Associate Professor Dept MBA
SISTAM ENGINEERING COLLEGE
SRIKAKULAM

TOPICS:

- Fundamentals and Regulations of entrepreneurship.
- Entrepreneurship: How to be?
- Innovation through idea generation.

**VENUE: BLOCK-C, AUDITORIUM,
SVCET**



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE-DAY WORKSHOP ON EMPOWERING ENTREPRENEURIAL ENDEAVORS FOSTERING INNOVATION THROUGH IDEA GENERATION

DATE:14/09/2018

LIST OF PARTICIPANTS

S.NO	REGD.NO	NAME OF THE STUDENT	BRANCH	SIGNATURE
1	16MT1A0501	ANNEPU YAMUNA	CSE-II	A. Yamuna
2	16MT1A0502	ADIMULAM HARICHANDANA	CSE-II	A. Harichandana
3	16MT1A0503	AKKERAPU ANOOSHA	CSE-II	A. Anoosha
4	16MT1A0504	AMPALAM SINDHUJA	CSE-II	A. Sindhuja
5	16MT1A0505	ANUSHA RANI SAHU	CSE-II	S. Anusha Rani
6	16MT1A0506	BAIKUNTHONATH BEHERA	CSE-II	B. Behera
7	16MT1A0508	BONDA SUBBALAMMA	CSE-II	B. Subbalamma
8	16MT1A0509	CHITTI SRAVANI	CSE-II	C. Chitravani
9	16MT1A0510	DEVADI GANESH	CSE-II	D. Ganesh
10	16MT1A0511	DUMPALA GOWRI SHANKAR	CSE-II	D. Gowri Shankar
11	16MT1A0512	DUVVADA PRASUNA	CSE-II	D. Prasanna
12	16MT1A0513	GANDHAM LAVANYA	CSE-II	G. Lavanya
13	16MT1A0514	GONDELA KUMARI	CSE-II	G. Kumari
14	16MT1A0515	GORLI ROHINI	CSE-II	G. Rohini
15	16MT1A0516	GORLI SANTHI	CSE-II	G. Santhi
16	16MT1A0517	GURRU VENU	CSE-II	G. Venu
17	16MT1A0518	V V S R P BHARADWAJ	CSE-II	V.V.S.R.P. Bharadwaj
18	16MT1A0519	KAGITAPILLI SIRISHA	CSE-II	K. Sirisha
19	16MT1A0520	K DHANALAKSHMI	CSE-II	K. Dhanalakshmi
20	16MT1A0521	KILLAMSHETTY NEERAJAKSHMI	CSE-II	K. Neerajakshmi
21	16MT1A0522	KONDAGORRI PAVAN	CSE-II	K. Pavan
22	16MT1A0523	KORRA SANTHI KUMARI	CSE-II	K. Santhi K. Kumari

23	16MT1A0525	KUPPILI SWATHI	CSE-II	Kuppili Swathi
24	16MT1A0526	KUTCHERLAPATI PRAMEELA	CSE-II	K. Prameela
25	16MT1A0528	MUDDADA SUSHMITA	CSE-II	M. Sushmita
26	16MT1A0531	NANDU KISHORE BANGARI	CSE-II	N. Kishore
27	16MT1A0532	OMMI HARINADH	CSE-II	O. Harinadh
28	16MT1A0533	PALAKA MOUNIKA	CSE-II	P. Mounika
29	16MT1A0535	PANDRANKI SUNEETHA	CSE-II	P. Suneetha
30	16MT1A0536	PILAKA RESHMA	CSE-II	P. Reshma
31	16MT1A0537	PIPPALLA SAILAJA	CSE-II	P. Sailaja
32	16MT1A0538	POLAKI HARITHA	CSE-II	P. Haritha
33	16MT1A0539	RAGOLU SUMATHI	CSE-II	R. Sumathi
34	16MT1A0540	SAVARA LOLAKSHI	CSE-II	S. Lolakshi
35	16MT1A0541	SERABA MOUNIKA	CSE-II	S. Mounika
36	16MT1A0542	SWETHA PRIYA SAHU	CSE-II	S. Swetha priya
37	16MT1A0543	TALABATTULA RAJESH	CSE-II	T. Rajesh
38	16MT1A0544	TANKALA SIREESHA	CSE-II	T. Sireesha
39	16MT1A0545	TRIPURANA DIVYA	CSE-II	T. Divya
40	16MT1A0546	TURAKA JAGADEESWARI	CSE-II	T. Jagadeeswari
41	16MT1A0547	YALAMANCHILI APARNA	CSE-II	X. Aparna
42	17MT5A0502	YENDA SRIVIDYA	CSE-II	Y. Srividya

IQAC

PROGRAMME COORDINATOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date: 15/09/2018

EDC Cell organised A **One-day Workshop on Empowering Entrepreneurial Endeavors fostering innovation through idea generation.**

The following member acted as a Resource person for the Workshop

Dr. K. Satyam, Associate Professor, Dept Of MBA, SARADA INSTITUTE OF SCIENCE TECHNOLOGY AND MANAGEMENT, Srikakulam, Andhra Pradesh.

The expert sessions covered various aspects of entrepreneurship through idea generation:

- **The Power of Collaboration:**

Collaboration is a key driver in revolutionizing business. By fostering collaborative ecosystems that bring together diverse talents, industries, and perspectives, organizations can accelerate innovation and create breakthrough solutions. Collaborative approaches enable knowledge sharing, cross pollination of ideas, and the pooling of resources, leading to greater collective impact. Embracing collaboration empowers businesses to tackle complex challenges and drive meaningful change.

- **Innovation:**

is the driving force behind successful businesses in today's rapidly evolving landscape. It enables organizations to adapt, stay ahead of the competition, and create value for customers. By fostering a culture of innovation, companies can unlock new opportunities, improve processes, and deliver groundbreaking products and services. Embracing innovation is essential for long-term success in the modern business world. All the Participants felt highly impressed and motivated of the seminar. They feel that it is a real time experience for them in participating in the seminar.

OBJECTIVES OF THE PROGRAM

The objective of the program is to bring awareness in the students about the future belongs to those who dare to challenge conventions, disrupt industries, and create new possibilities. Let us embark on this transformative journey together and shape the future of business.

OUTCOMES OF THE PROGRAM

All the Students are awareness about Enhance understanding of entrepreneurship, idea generation, identifying opportunities importance of knowledge about entrepreneurial practices in the corporate world



Program Coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com



Prasad

Signature of the program coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

CIRCULAR

Date:25/09/2018

This is to inform to all the students that the EDC cell is going to conduct **A One-Day Workshop on Risk management in entrepreneurship a path to success.**

The workshop aimed to make students aware of risk and reward in entrepreneurship navigating the critical implications of embracing or avoiding risks.

Coordinator : MR .P. VENKATESWARA RAO, Asst.Prof, Dept. of MBA,SVCET.

Resource Person: CH. KEERTHANA, Asst.professor, Dept. Of MBA, SSCE
Engineering College, Srikakulam

Date : 4th -OCT-2018






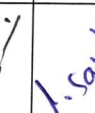

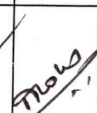

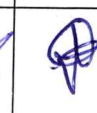
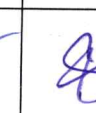
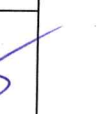
All the students must attend the seminar in the seminar hall at 9:30am with the proper dress code & ID cards.


Programme Coordinator


Principal

PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Copy submitted to: Honourable Management for favour of information

Department	IQAC	CE	EEE	ME	ECE	CSE	MBA	BS&H	EDC	NOTICE BOARD	OFFICE	FILE
Signature												

PROGRAMME SCHEDULE:

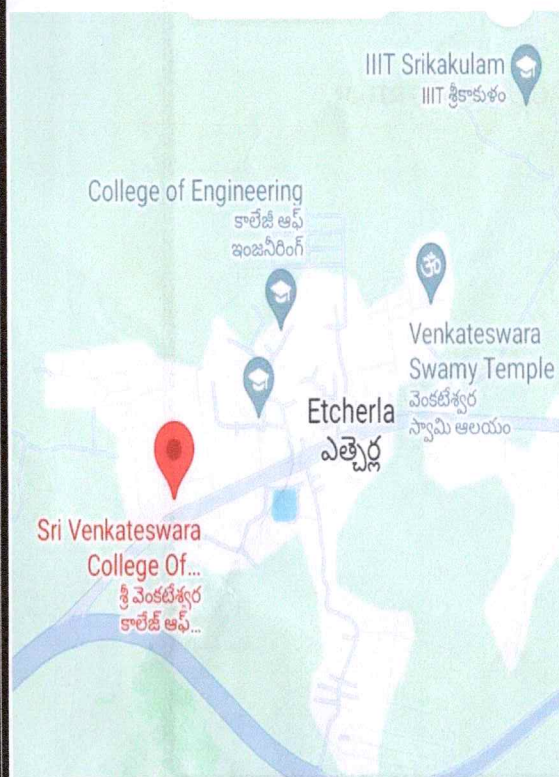
09:30 a.m. to 10:00 a.m.	: Registration
10:00 a.m. to 11:00 a.m.	: Inauguration
11:00 a.m. to 11:15 a.m.	: Tea Break
11:15 a.m. to 01:00 p.m.	: Lecture – I
01:00 p.m. to 02:00 p.m.	: Lunch Break
02:00 p.m. to 03:30 p.m.	: Lecture – II
03:30 p.m. to 03:45 p.m.	: Tea Break
03:45 p.m. to 04:30 p.m.	: Valedictory

Conveners:

Smt.V.SANTHI, Assistant Professor, HOD-MBA

Mr. L.PRASADA RAO, Assistant Professor, Dept. of MBA.

ROUTE MAP:



ORGANIZING COMMITTEE

Chief Patron:

Dr.B.SRI RAMA MURTHY

Secretary,VES

Patrons:

Dr. M.GOVINDA RAJU, Principal, SVCET

Co-Patron

Sri I.Kishore, Director,VES,ISVC

Dr.B.S.RAJA, Director,VES

Sri B.Raghu Karthik, Director

Sri B.Krishna Vamsi, Director

Convener:

Smt. V. santhi, Assistant professor Dept of HOD

coordinator:

Mr. P. Venkateswara Rao, Asst Prof.

Dept of MBA

Faculty Coordinators:

Dr P.RAVI KUMAR,Asst.professor,M.B.A
Mr.M.RAJA MOHAN,Asst.professor,M.B.A
Mr.P.VENKATSWARA RAO, Asst.professor,M.B.A
Mr.S.SRIKANTH,Asst.professor,M.B.A
Mr.A.NAVEENA,Asst.professor,M.B.A
Mr.K.CHANDRA SEKHAR,Asst.professor,M.B.A
Mr.K. DURAGARAO, Asst Professor, MBA
Mr. S.BHASKAR RAO, Asst. Prof. C.S.E
Mr.B.MURALI, Asst. Prof. E.E.E
Mr.K ESWARA RAO, Asst. Prof. Mech
Mr.N.SIMHAHALAM, Asst. Prof. Civil
Mr.V.JEEVAN KUMAR, Asst. Prof. E.C.E

A ONE DAY WORKSHOP

ON

TITLED RISK MANAGEMENT IN
ENTREPRENEURSHIP A PATH TO
SUCCESS

4th October, 2018



Organized by
ENTREPRENEURSHIP DEVELOPMENT CELL

SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY
Accredited by NAAC "A" GRADE
(Affiliated by JNTUK Kakinada & Approved by AICTE)
ETHERLA, SRIKAKULAM.
ANDHRA PRADESH. 532 410
www.svcet.info

ABOUT THE COLLEGE:

Sri Venkateswara College of Engineering & Technology, Etcherla, Srikakulam (Dist.) was established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. SVCET is accredited by NAAC in 2016. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow end owed with highly qualified and seasoned faculty on its rolls. Sri Venkateswara College of Engineering & Technology is committed to "Impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values. The Institute is a recipient of Best Green Campus award for 2017 by Govt. of A.P. in 2018.

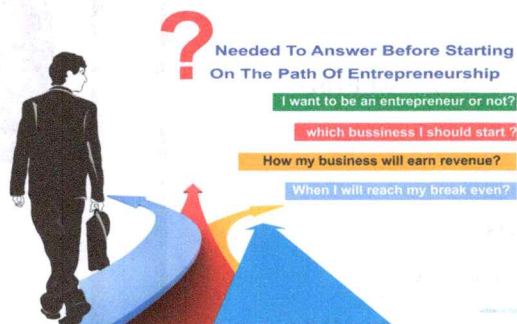
ABOUT EDC:

Entrepreneurship Development Cell (EDC) is being promoted in SVCET to develop institutional mechanism to create entrepreneurial culture and to foster entrepreneurship for generation of wealth and employment. To guide and assist prospective entrepreneurs on various aspects such as preparing project reports, obtaining project approvals, loans and facilities from agencies of support system, information on technologies, To arrange interaction with entrepreneurs and create a mentorship scheme for student entrepreneurs.

Cell is a core area in an Engineering College which complements the efforts of the members of faculty by organizing value-added programs and providing placement opportunities to students. The cell continuously coordinates with various industries and organizations for placement of the students.

ABOUT THE WORKSHOP:

A workshop on 'Titled Risk management in Entrepreneurship a path to success' is main the objective of the program is to bring awareness in the students about Finding the right balance between embracing and avoiding risks is crucial. It requires effective risk management strategies, data-driven decision making, and a culture of accountability.



RESOURCE PERSON AND TOPICS

Resource Persons:

CH. KEERTHANA
Assistant Professor
SRI SHIVANI COLLEGE ENGINEERING
SRIKAKULAM

TOPICS:

- various aspects of Risk & Reward in Entrepreneurship:
- How to Avoiding risks
- Embracing risks

**VENUE: BLOCK-C, AUDITORIUM,
SVCET**



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE-DAY WORKSHOP ON TITLED RISK MANAGEMENT IN ENTREPRENEURSHIP A PATH TO SUCCESS

DATE:04/10/2018

LIST OF PARTICIPANTS

S.NO.	REGD.NO.	NAME OF THE STUDENT	BRANCH	SIGNATURE
1	18MT1A0401	ADIMULAM PRASANTH KUMAR	ECE-I	A. Prasanth
2	18MT1A0402	BHASKARABHATLA CHINMAI	ECE-I	B. chinmai
3	18MT1A0403	BURLE ANURADHA	ECE-I	B. Anuradha
4	18MT1A0404	CHALAPATI MEGHANA	ECE-I	C. Meghana
5	18MT1A0405	DHARMANA SRAVANI	ECE-I	D. Sravani
6	18MT1A0406	DUKKA DEVI	ECE-I	D. devi
7	18MT1A0407	EASAI BHAGYA LAXMI	ECE-I	E. Bhagya laxmi
8	18MT1A0408	GEDALA MARY MANI	ECE-I	G. Mary mani
9	18MT1A0409	GUNA TEJESWARI	ECE-I	G. Tejeswari
10	18MT1A0410	GUNTREDDI SUSMITHA	ECE-I	G. Susmitha
11	18MT1A0411	GURUGUBELLI VENKATA HYMAVATHI	ECE-I	G. venkata hyman
12	18MT1A0412	JANAM VENUGOPAL	ECE-I	J. Venugopal
13	18MT1A0413	JARJANA YAMUNA	ECE-I	J. Yamuna
14	18MT1A0414	KAPPARAPU RAMU	ECE-I	K. Ramu
15	18MT1A0415	KODEM SAI MANIKANTA PAVAN KUMAR	ECE-I	K. Pavan
16	18MT1A0416	KONDAGORRI AMEELA	ECE-I	K. Ameela
17	18MT1A0417	KONDALA SRAVANI	ECE-I	K. Sravani
18	18MT1A0418	KUNDANGI PAVITRA	ECE-I	K. Pavitra
19	18MT1A0419	LINGALA SANDHYA	ECE-I	L. Sandhya
20	18MT1A0420	LINGALA VENUKUMARI	ECE-I	L. Venkumari
21	18MT1A0421	LOPINTI SANDHYARANI	ECE-I	L. Sandhyarani
22	18MT1A0422	MAMIDI SWATHI	ECE-I	M. Swathi
23	18MT1A0423	MARNUR AKASH	ECE-I	M. Akash
24	18MT1A0424	MURLA SAI KUMARI	ECE-I	M. Saikumari
25	18MT1A0425	NAIDU BHARGAVI	ECE-I	N. Bhargavi
26	18MT1A0426	NANNI ASHADOLAI	ECE-I	N. Ashadolai
27	18MT1A0427	PANDURU RAKESH	ECE-I	P. Rakesh

28	18MT1A0428	PANGI MAMATHA	ECE-I	P. Mamatha
29	18MT1A0429	PASUPUREDDY GOWTHAMI	ECE-I	P. Gowthami
30	18MT1A0430	PATHINA DANTESWARA RAO	ECE-I	P. Danteswara Rao
31	18MT1A0431	PATTIKA MANISHA	ECE-I	P. Manisha
32	18MT1A0432	RAGHUPATRUNI SAI SANTOSHIDARSHINI	ECE-I	R. Sai Santoshid Arshini
33	18MT1A0433	ROUTHU SAIKUMAR	ECE-I	R. Saikumar
34	18MT1A0434	SANTRO SUMAN KUMAR	ECE-I	S. Suman Kumar
35	18MT1A0435	SAVALAPURAPU SUKANYA	ECE-I	S. Sukanya
36	18MT1A0436	SIGATAPU PRADEEPTHI	ECE-I	S. Pradeepthi
37	18MT1A0437	VADAGA RAVITEJA	ECE-I	V. Raviteja
38	18MT1A0438	VENKATA SATYANARAYANABADANA	ECE-I	Venkata satynaryanane
	18MT1A0439	YALAGADA RENUKA	ECE-I	Y. Renuka
40	18MT1A0440	YANAPU SANDEEP	ECE-I	Y. Sandeep
41	18MT1A0441	YANDA MOUNIKA	ECE-I	Y. Mounika
42	18MT1A0442	YEJALA GAYATHRI	ECE-I	Y. GAYATHRI
43	18MT1A0443	SANA MURALI KRISHNA	ECE-I	S. murali krishna
44	18MT1A0444	SEERA HARITHA SREE	ECE-I	S. Haritha sree
45	18MT1A0445	ANAPANA VASANTH	ECE-I	A. vasanth.

Jan
IQAC

DL
PROGRAMME COORDINATOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date: 05/10/2018

EDC cell is organise **A One-Day Workshop on Risk management in entrepreneurship a path to success.**

The following member is invited as a Resource person for the Workshop

CH. KEERTHANA, Asst.professor, Dept. Of MBA, SRI SHIVANI COLLGE OF ENGINERING, Srikakulam, Andhra Pradesh

highlighted the importance of Risk & Reward in Entrepreneurship Navigating the Critical Implications of embracing or Avoiding Risks.

The expert sessions covered various aspects of Risk & Reward in Entrepreneurship:

1. Risks are uncertain events that can have positive or negative outcomes. They can arise from internal or external factors and must be carefully evaluated and managed.
2. Embracing risks can lead to innovation and growth. It encourages creativity and opportunity-seeking behaviour, enabling organizations to stay competitive in a rapidly changing environment.
3. Avoiding risks can provide stability and security. It focuses on risk mitigation and ensuring continuity of operations. However, excessive risk avoidance can hinder progress and limit potential rewards.

All the Participants felt highly impressed and motivated of the seminar. They feel that it is a real time experience for them in participating in the seminar.

OBJECTIVES OF THE PROGRAM

The objective of the program is to bring awareness in the students about Finding the right balance between embracing and avoiding risks is crucial. It requires effective risk management strategies, data-driven decision making, and a culture of accountability.

OUTCOMES OF THE PROGRAM

All the students who are participate in the program have felt free and realized Achieving success in a dynamic business environment necessitates understanding and navigating the critical implications of embracing or avoiding risks. By striking the right balance, organizations can realize opportunities, drive innovation, and maximize rewards while managing potential downsides


Program Coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com




Signature of the program coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

CIRCULAR

Date 12-12-2018

This is to inform to all the students of B-Tech that we are going to conduct **A One Day Seminar On The Challenges Of Intellectual Property Rights And Protection Of Human Rights With Special Reference To Sustainable Development** on 19/12/2018 [10A.M to 4P.M] All the students have to report at the seminar hall sharp at 9:30A.M with the proper dress code and with ID cards. Attendance is mandatory.

Resource persons: Dr. J. Bala Bhaskara Rao, Professor, SSCE, Srikakulam

All the students have to attend the seminar in the digital hall of the BS&H department at 9:30A.M

This program is aimed to: The participant can learn, Specific Intellectual Property Tools (Patents, Copyrights, Design rights, Trade secrets and trade marks,) and Their Contribution to SDGs


Program coordinator

Submitted to: Honorable Management for Favor of Information


Principal
PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Copy To:

Department.	IQAC	CE	EEE	ME	ECE	CSE	BS&H	MBA	OFFICE	NB	FILE
Signature											

About the Institution:

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

About the Department:

Computer Science and Engineering is a profession that uses science, technology and problem solving skills will acquire knowledge of the design, implementation, and management of information systems of both hardware and Software. The department of CSE was established in 2008 with aim of importing high quality technical education in the area with an intake of 60.

CHIEF PATRON:

Dr. B. SRI RAMA MURTHY,
SECRETARY,
VIKAS EDUCATIONAL SOCIETY

PATRON:

Dr. M. GOVINDHA RAJU
Principal, SVCET

CO-ORDINATOR:

Mr. M. Lakhsman Rao
Asst. Professor, CSE Dept
CELL: 9441853589

RESOURCE PERSON:

Dr. J. Bala Bhaskara Rao
Professor
Department of Mechanical Engineering
SSCE, Srikakulam

A ONE DAY SEMINAR ON THE CHALLENGES OF INTELLECTUAL PROPERTY RIGHTS AND PROTECTION OF HUMAN RIGHTS WITH SPECIAL REFERENCE TO SUSTAINABLE DEVELOPMENT

19-12-2018



Organized by

**DEPARTMENT OF COMPUTER SCIENCE AND
ENGINEERING**

**SRI VENKATESWARA COLLEGE OF
ENGINEERING AND TECHNOLOGY**

(Under the Management of Vikas Educational
Society, Srikakulam)

Accredited by NAAC with 'A' Grade
(Approved by AICTE, New Delhi & Affiliated to
JNTUK, Kakinada; G.O.Ms.No.101, Dt. 09-07-08)
NH-5, ETCHERLA-532410, SRIKAKULAM (Dt)

ANDHRA PRADESH

Website: www.svcet.info,

Email: principal_svcet@yahoo.com

Department Vision:

Our vision is to be a global leader in computer science and engineering education, research, and innovation.

Department Mission:

Our mission is to advance the frontiers of computer science and engineering through excellence in education, research, and innovation. We are committed to providing our students with a comprehensive and cutting-edge education that prepares them to become leaders and innovators in the rapidly evolving field of computing.

Objective:

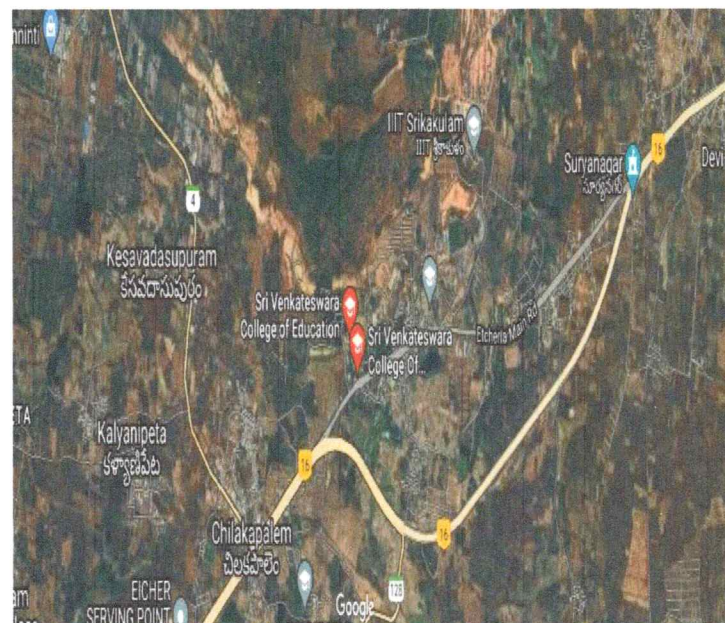
The objective of the program is to raise awareness about the potential conflicts and synergies between intellectual property rights and human rights, particularly in the context of promoting sustainable development.

Expected Outcomes:

Participants should be able to formulate informed policy recommendations aimed at promoting a more balanced and inclusive approach to intellectual property rights that respects and protects human rights while advancing sustainable development goals.



Route Map:



Sri Venkateswara College of Engineering and Technology
(Under the Management of Vikas Educational Society, Srikakulam)

REGISTRATION FORM

“A ONE DAY SEMINAR ON THE CHALLENGES OF INTELLECTUAL PROPERTY RIGHTS AND PROTECTION OF HUMAN RIGHTS WITH SPECIAL REFERENCE TO SUSTAINABLE DEVELOPMENT”

19-12-2018

Participant's name :

Gender: Male/Female :

Year & Sem :

Branch :

Organization :

Address for Communication :

Mobile Number :

Email ID :

Date: **Signature of the Participant**

Signature of the HOD



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON THE CHALLENGES OF INTELLECTUAL PROPERTY RIGHTS AND PROTECTION OF HUMAN RIGHTS WITH SPECIAL REFERENCE TO SUSTAINABLE DEVELOPMENT

List of Enrolled Students with Signature

Date: 19-12-2018

S-NO	NAME OF THE PARTICIPANT	REDG-NO	BRANCH	SINGNATURE
1	AMPOLU THARUN KUMAR	15MT1A0201	EEE-IV	A. Tharun Kumar
2	ANNEPU ESWARAMMA	15MT1A0202	EEE-IV	A. Eswaramma
3	BADIYA ARUNA	15MT1A0204	EEE-IV	B. ARUNA
4	BALIVADA KRISHNA KUMARI	15MT1A0205	EEE-IV	B. Krishnakumari
5	BANALA SANTHOSHI	15MT1A0206	EEE-IV	B. Santhoshi
6	BONELA SRIKANTH	15MT1A0207	EEE-IV	B. SRIKANTH
7	DABBADA TRINADHA RAO	15MT1A0209	EEE-IV	D. Trinadha Rao
8	DATTI TEJESH	15MT1A0210	EEE-IV	D. TEJESH
9	DEVU KAVYA KUMARI	15MT1A0211	EEE-IV	D. Kavita Kumari
10	DUNDANGI URMILA	15MT1A0212	EEE-IV	U. URMILA
11	GUNTA PAVANI	15MT1A0213	EEE-IV	G. PAVANI
12	HANUMANTHU PRASANNA KUMAR	15MT1A0214	EEE-IV	H. PRASANNA KUMAR
13	KUNDANGI SUNDARA RAO	15MT1A0218	EEE-IV	K. SUNDARA RAO
14	NARSIPURAM SAIBHARATH	15MT1A0220	EEE-IV	N. Saibharath
15	NAYANA MANIKANTAREDDY PAIDI	15MT1A0221	EEE-IV	N. Manikanta Reddy
16	VENKATASURYARAO	15MT1A0223	EEE-IV	V. Suryarao
17	POONDRU SWATHI	15MT1A0225	EEE-IV	P. Swathi
18	PUJARI JYOTHSNA	15MT1A0226	EEE-IV	P. Jyothsna
19	PURRU GANESH	15MT1A0227	EEE-IV	P. Ganesh
20	RAVADA DHARMA RAO	15MT1A0228	EEE-IV	R. Dharmarao
21	SAVARA KAVITHA	15MT1A0229	EEE-IV	S. Kavitha
22	SHANMUKHA RAO PILAKA	15MT1A0230	EEE-IV	Shanmukha Rao P.
23	SILAM SAI MOHAN	15MT1A0231	EEE-IV	S. Sai Mohan
24	THOLAPU SAI KUMAR	15MT1A0233	EEE-IV	T. Sai Kumar
25	YADLA NAVEEN	15MT1A0234	EEE-IV	Y. Naveen

26	YAMALA NARAYANA RAO	15MT1A0235	EEE-IV	y. Narayana.
27	BARATAM HARITHA	16MT5A0201	EEE-IV	B. haritha
28	BARATAM SAIKIRAN	16MT5A0202	EEE-IV	B. Sai Kiran
29	BHADRASI SAIRAM	16MT5A0203	EEE-IV	B. Sairam
30	GUNDA RASHMI	16MT5A0206	EEE-IV	G. Rashmi
31	KANITHI SATISH KUMAR	16MT5A0207	EEE-IV	K. Satish Kumar
32	KETHA RAHUL	16MT5A0208	EEE-IV	K. Rahul
33	KUMILI SUNIL KUMAR	16MT5A0209	EEE-IV	K. Sunil Kumar
34	MALLAJOSYULA SIVA RAMA KRISHNA SASTRY	16MT5A0210	EEE-IV	M. Josyula Siva Ramakrishna.
35	RAYALA GNANESH	16MT5A0212	EEE-IV	R. Gnanesh

Jms
IQAC

M. Laxmi
PROGRAM CO-ORDINATOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date 21-12-2018

It is to inform that **A One Day Seminar On The Challenges Of Intellectual Property Rights And Protection Of Human Rights With Special Reference To Sustainable Development** has been conducted during 19th Dec 2018 in the Digital hall of BS&H department.

RESOURCE PERSON:

Dr. J. Bala Bhaskara Rao

Professor

Department of Mechanical Engineering

SSCE, Srikakulam

CO-ORDINATOR:

Mr. M Lakhsman Rao

Assistant Professor

CSE Dept

SVCET

Number of Students Participated: 35

OBJECTIVES OF THE PROGRAM

The objective of the Intellectual Property Rights (IPR), with a concentrated focus on copyrights and patents, is to provide participants with a comprehensive understanding of the legal, economic, and societal implications of intellectual property protection in the digital age. Through insightful discussions, expert presentations, and interactive sessions, the seminar aims to explore the evolving landscape of copyrights and patents, including recent legislative developments, emerging challenges, and best practices. By fostering interdisciplinary dialogue and knowledge

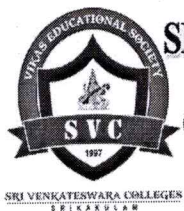
exchange among legal experts, industry professionals, policymakers, and academics, the seminar seeks to empower participants with the tools, strategies, and insights needed to navigate the complexities of intellectual property management, protect their creative and innovative endeavors, and contribute to the advancement of a balanced and inclusive intellectual property regime that fosters innovation, creativity, and sustainable development.

OUTCOMES OF THE PROGRAM

Attendees should acquire a comprehensive understanding of copyright law, including what types of works are eligible for copyright protection, the rights granted to copyright holders, the duration of copyright protection, and the concept of fair use.



M. Lakshmi
Signature of the program coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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Web : www.svcet.info, Email : principal_svcet@yahoo.com

CIRCULAR


Date 20-01-2019

This is to inform to all the students of B-Tech that we are going to conduct **A One Day Seminar On Potential Challenges Faced In Implementing Intellectual Property Rights In India** on **23/01/2019** [10A.M to 4P.M] All the students have to report at the seminar hall sharp at 9:30A.M with the proper dress code and with ID cards. Attendance is mandatory.

Resource person: Dasari. Tirumala Rao, Associate Professor, SVCET, Etcherla, Srikakulam.

All the students have to attend the seminar hall of the BS&H department at 9:30A.M












This program is aimed to: The students can learn the concept of IPR, patent ever greening prevention, subsidies & IPR issues and also the product patents process.


Program coordinator


Principal

PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Copy To: Honorable Management for Favor of Information:

Department	IQAC	CE	EEE	ME	ECE	CSE	BS&H	MBA	OFFICE	NOTICE BOARD	FILE
Signature											

About the Institution:

Sri Venkateswara College of Engineering & Technology ETCHERLA was established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

About the Department:

The Mechanical Department typically refers to the division within an organization or academic institution that focuses on mechanical engineering. Mechanical engineering is a broad field that involves the design, development, and maintenance of mechanical systems and components. This can include everything from engines and machinery to heating and cooling systems, robotics, and manufacturing processes.

HIEF PATRON:

DR B. SRI RAMA MURTHY,
CORRESPONDENT,
SRI VENKATESHWARA COLLEGES

PATRON:

DR. M. GOVINDA RAJU
Principal, SVCET

CO-ORDINATOR:

MR. L. Mohan Rao
Asst. Professor
ME DEPT
CELL: 9885947733

RESOURCE PERSONS:

Mr. D. Tirumala Rao
Associate Professor
Department of ECE
SVCET, Srikakulam

**A ONE DAY SEMINAR ON POTENTIAL
CHALLENGES FACED IN IMPLEMENTING
INTELLECTUAL PROPERTY RIGHTS IN
INDIA**

ON 23-01-2019



Organized by

**DEPARTMENT OF MECHANICAL
ENGINEERING**

**SRI VENKATESHWARA COLLEGE OF
ENGINEERING AND TECHNOLOGY**
(Under the Management of Sri Vikas Educational
Society, Srikakulam)
(Accredited by NAAC With 'A' Grade(Approved
by AICTE, New Delhi; Affiliated to JNTUK,
Kakinada; G.O.Ms.No.101, Dt. 09-07-08)
NH-5, ETCHERLA-532410, SRIKAKULAM
(Dist.), ANDHRA PRADESH
Website: www.svcet.info,
Email : principal_svcet@yahoo.com

Department Vision:

To be a global leader in advancing mechanical engineering knowledge, innovation, and sustainable solutions for the betterment of society.

Department Mission:

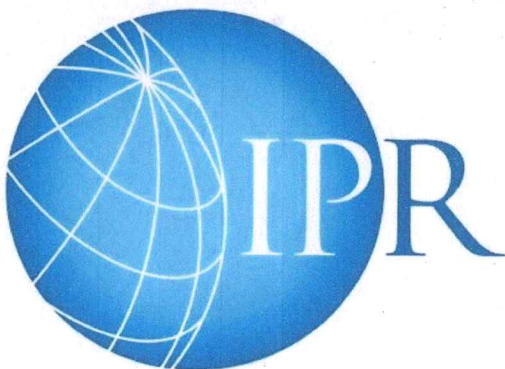
Provide a dynamic learning environment that fosters creativity, critical thinking, and practical skills in mechanical engineering, preparing students to excel in their careers and contribute to society.

Objective:

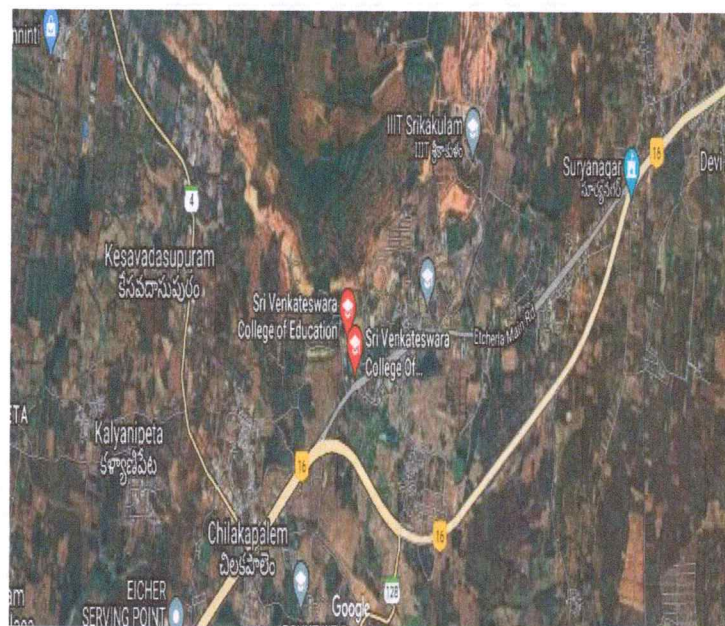
The objective of the program is to identify and analyze the key challenges and barriers hindering the effective implementation of intellectual property rights in India, considering legal, institutional, cultural, and socio-economic factors.

Expected Outcomes:

Participants should be inspired to take concrete actions within their spheres of influence to contribute to the effective implementation of intellectual property rights in India, thereby supporting innovation, economic growth, and social development.



Route Map:



REGISTRATION FORM

“A ONE DAY SEMINAR ON POTENTIAL CHALLENGES FACED IN IMPLEMENTING INTELLECTUAL PROPERTY RIGHTS IN INDIA”

23-01-2019

Participant's name :

Gender: Male/Female :

Educational Qualification :

Department :

Organization:

Address for Communication :

Mobile Number :

Email ID :

Date: Signature of the Participant

Signature of the HOD



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH


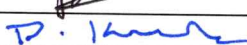
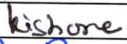

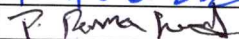
Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON POTENTIAL CHALLENGES FACED IN IMPLEMENTING INTELLECTUAL PROPERTY RIGHTS IN INDIA

List of Enrolled Students with Signature

Date: 23-01-2019

S-NO	NAME OF THE PARTICIPANT	REDG-NO	BRANCH	SINGNATURE
1	ABOTHULA RAMBABU	15MT1A0301	MECH-IV	A. Rambabu
2	ADAPAKA RAMAGANESH	15MT1A0302	MECH-IV	A. Ramaganes
3	ALIJINGI KALYAN	15MT1A0303	MECH-IV	A. Kaljani
4	ANAPANA PAVANKUMAR	15MT1A0304	MECH-IV	A. Pavankumar
5	ARJALA SURENDRA	15MT1A0305	MECH-IV	A. Surendra
6	ATTI RAMKISHORE	15MT1A0306	MECH-IV	A. Ramkishore
7	BADI CHANDRA SEKHAR	15MT1A0307	MECH-IV	B. Sekhar
8	BAMMIDI TARAKESWARA RAO	15MT1A0309	MECH-IV	B. Tarakes
9	BANGARI CHATAN KUMAR	15MT1A0310	MECH-IV	B. Chatur
10	BUDDEPU DILLESWARARAO	15MT1A0311	MECH-IV	B. Dilleswar
11	BUDUMURU KRISHNA MURTY	15MT1A0312	MECH-IV	B. Krishna
12	BUSAKALA VENUGOPAL	15MT1A0313	MECH-IV	B. Venugop
13	CHIKKALA TARAKESH	15MT1A0314	MECH-IV	C. Tarakesh
14	CHINNARI MANIKANTA	15MT1A0315	MECH-IV	C. Manikanta
15	DAKKATA BALARAJU	15MT1A0317	MECH-IV	D. Balaji
16	DIVVALASA VENUGOPALA RAO	15MT1A0318	MECH-IV	D. Venugop
17	DUKKA GOWRIKUMAR	15MT1A0320	MECH-IV	D. Gowri
18	DUPANA PREMKUMAR REDDY	15MT1A0321	MECH-IV	D. Premkumar
19	GARA DILLESWARA RAO	15MT1A0323	MECH-IV	G. Dilleswar
20	GULLA JHONY	15MT1A0326	MECH-IV	G. Jhony
21	HANUMANTHU BABJI	15MT1A0328	MECH-IV	H. Babji
22	HIMA SEKHAR AKKIREDDY	15MT1A0329	MECH-IV	H. Sekhar
23	KAKI SANKARA RAO	15MT1A0330	MECH-IV	K. Sankara
24	KARUMOJU MANOJ KUMAR	15MT1A0333	MECH-IV	K. Manoj
25	KEMBURU ATCHUTA RAO	15MT1A0335	MECH-IV	K. Atchuta
26	KONDAGORRI CHANTI	15MT1A0336	MECH-IV	K. Chanti
27	KUNCHI SIVAJI	15MT1A0337	MECH-IV	K. Sivaji
28	LANKAVALASA DURGAPRASAD	15MT1A0338	MECH-IV	L. Durgapras
29	LINGALA APPANNA	15MT1A0339	MECH-IV	L. Appanna
30	MACHIRAJU RAGHAVA RAO	15MT1A0340	MECH-IV	M. Raghava
31	MEKALA SIVA	15MT1A0343	MECH-IV	M. Siva
32	NANUPURAM GOPICHAND	15MT1A0344	MECH-IV	N. Gopichand
33	NARAYANASETTY LOKESH KUMAR	15MT1A0345	MECH-IV	N. Lokesh
34	NARSIPURAM MOHANAVAMSI	15MT1A0346	MECH-IV	N. Mohanav
35	NEELAPU TARAKESH	15MT1A0348	MECH-IV	N. Tarakesh

36	PAILA RAMBABU	15MT1A0349	MECH-IV	
37	PALISETTI KISHORE	15MT1A0351	MECH-IV	
38	PENKI KISHORE	15MT1A0353	MECH-IV	
39	PERLA PAULBHUSHAN	15MT1A0354	MECH-IV	
40	PILA RAMA KRISHNA	15MT1A0355	MECH-IV	


IQAC


PROGRAM CO-ORDINTOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

PROGRAM REPORT

Date 24-01-2019

It is to inform that **A One Day Seminar on Potential Challenges Faced in Implementing Intellectual Property Rights in India** has been conducted during 23rd Jan 2019 in the Digital hall of BS&H department.

RESOURCE PERSON:

Dasari Tirumala Rao
Assoc. Professor
SVCET,
Etcherla, Srikakulam.

CO-ORDINATOR:

Mr. L. Mohan Rao
Assistant Professor in Mechanical Engineering department,
SVCET

Number of Students Participated: 40

OBJECTIVES OF THE PROGRAM

The objective of the one-day seminar on "Potential Challenges Faced in Implementing Intellectual Property Rights in India" is to comprehensively analyze the obstacles and complexities hindering the effective implementation of intellectual property rights (IPRs) within the Indian context. By bringing together stakeholders from academia, industry, government, and legal sectors, the seminar aims to identify key challenges, explore underlying causes, and discuss potential solutions to strengthen the enforcement and protection of intellectual property in India.



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

OUTCOMES OF THE PROGRAM

The seminar will shed light on enforcement issues such as piracy, counterfeiting, patent infringement, and challenges related to the judicial process, providing insights into the practical difficulties faced by rights holders in safeguarding their intellectual property.



Signature of the program coordinator



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

Date: 04/03/2019

CIRCULAR

This is to inform to all the students of B.Tech. that the Dept. of EEE is going to conduct A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING On 6th March 2019 [10.00 am to 04.00pm].

Resource Person : Dr.P.Devendra, Professor in Department of EEE GVPCEW, Visakhapatnam

Date : 6th-March-2019












All the students have to report at the seminar hall sharp at 09:45am with the proper dress code and with ID cards. Attendance is mandatory.

Venue: Seminar Hall, Block-A


PROGRAM COORDINATOR


PRINCIPAL
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Submitted to: Honorable Management for favor of information

Department	IQAC	CIVIL	EEE	MECH	ECE	CSE	BS&H	MBA	OFFICE	Notice Board	File
Signature											

ABOUT THE PROGRAM

Objective:

The primary objective of this one-day seminar is to provide a comprehensive platform for students in the field of latest research findings and technological advancements in electrical engineering. The seminar aims to advance the field of electrical engineering, contribute to the professional development of participants, and promote a collaborative and innovative research environment

Expected Outcomes:

The Seminar will not only advance the individual careers of participants but also contribute to the overall progress and innovation and Research within the field of Electrical Engineering. All The Students Who Have Participated In A One Day Seminar On “**A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING**” Have Felt Free And Realized The Importance Of Research And Very Much Enthusiastic To Get Them Absorbed In Research After Completion Of Their Studies.

****Spot Registration is available****

CHIEF PATRON

Dr.B.Sri Rama Murthy
Secretary,SVC

PATRON

Dr M.Govinda Raju
Principal,SVCET

CONVENER

A.Apparao
HOD, EEE Dept
Email id:3esvcet@gmail.com

COORDINATOR

J.Hareesh
Assistant Professor
EEE Dept,SVCET

A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING

6th-March-2019



Organized by
**DEPARTMENT OF
ELECTRICAL & ELECTRONICS
ENGINEERING**

**SRI VENKATESWARA COLLEGE OF
ENGINEERING & TECHNOLOGY**

Accredited by NAAC“A”Grade
(Affiliated to JNTUK Kakinada & Approved by
AICTE)

**Andhra Pradesh- 532410
Phone:+91-9440220356**

www.svcet.info

ABOUT THE INSTITUTION

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

ABOUT THE DEPARTMENT

Electrical and Electronics Engineering is a profession that uses science, technology and problem solving skills to design construct and maintain products. It is a challenging but exciting and rewarding area to study. The department of EEE was established in 2008 with aim of importing high quality technical education in the area of Electrical and Electronics Engineering with an intake of 60. The department offers post graduate program in the specialization of Power Electronics and control systems. The department of EEE offers many courses that can be chosen as electives by the students of the other departments. The curriculum has been designed to take the college to levels on par with advanced institutes.

The EEE Department has well qualified and experienced faculty members supported by trainers. The department adopts the right blend of practical approaches in training the students and placements.

DEPARTMENT VISION

To be a globally recognized center of excellence in Electrical and Electronic Engineering, pioneering innovations that shape the future of technology and society.

DEPARTMENT MISSION

Our mission is to provide transformative education, conduct cutting-edge research, and faster innovation in Electrical and Electronics Engineering to address global challenges and improve quality of life.

RESOURCE PERSON

Dr.P.Devendra

Professor in Department of EEE,
G.V.P College of Engineering for Women,
Visakhapatnam
Email id:devendra.p@gvpcew.ac.in

REGISTRATION FORM

"A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING"

6th-March-2019

Name: _____
ROLL NO: _____
Institution: _____
Mobile No: _____
Email: _____

Date: _____ Signature of the Participant

SPONSORSHIP CERTIFICATE

Mr./Ms. _____ is an Student of this institution and he/she is sponsored to attend the Seminar on **"A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING"**

**Signature of the HOD
With Seal**



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

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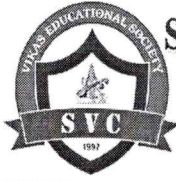
Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING

List of Participants

Date:06/03/2019

S. No	Regd. No	Name of the Student	Year & Sem	Signature
1	17MT1A0201	BADANA CHINNA RAO	EEE-II	B. Chinna Rao
2	17MT1A0202	BANDARU MOHANA RAO	EEE-II	B. Mohana Rao
3	17MT1A0203	BOKKISA VENKATESH	EEE-II	B. Venkatesh
4	17MT1A0204	BONTHU RAVI	EEE-II	B. Ravi
5	17MT1A0205	BOYINA DEVI	EEE-II	B. Devi
6	17MT1A0206	CHINTALA VENKATASAI KUMAR	EEE-II	C.V. Kumar
7	17MT1A0207	DHAVALA KRISHNA RAO	EEE-II	D. Krishna Rao
8	17MT1A0208	GADDAPU ASWINI	EEE-II	G. Aswini
9	17MT1A0209	GOLAGANA PAVAN KALYAN	EEE-II	G. Pavan Kalyan
10	17MT1A0211	GURUGUBELLI DILEEP KUMAR	EEE-II	G. Dileep Kumar
11	17MT1A0212	GURUGUBELLI KIRANMAYEE	EEE-II	G. Kiranmayee
12	17MT1A0214	PATHINA JALAJAKSHI	EEE-II	P. Jalajakshi
13	17MT1A0215	PEYYALA JYOTSHNA	EEE-II	P. Jyotshna
14	17MT1A0216	PILLA SYAM BABU	EEE-II	P. Syam Babu
15	17MT1A0217	PUTHI ATCHUTHA RAO	EEE-II	P. Atchutha Rao
16	17MT1A0218	REDDY MAHESH	EEE-II	R. Mahesh
17	17MT1A0221	UNKILI LAVANYA	EEE-II	U. Lavanya



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NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

18	17MT1A0222	UPPADA RAMYA	EEE-II	U. Ramya
19	18MT5A0201	BADI VINOD KUMAR	EEE-II	B.V. Kumar
20	18MT5A0202	BENDI BHANUCHANDAR	EEE-II	B. Bhanuchandara
21	18MT5A0203	BODDEPALLI RAJASEKHAR	EEE-II	B. Rajasekhara
22	18MT5A0204	DARAPU DHANA LALKSHMI	EEE-II	D. Rambabu
23	18MT5A0206	ESUKALAPALEPU RAMBABU	EEE-II	E. Rambabu
24	18MT5A0207	GORLE INDRAJA	EEE-II	G. Indraga
25	18MT5A0208	GURUGUBELLI VENKATA DURGA RAO	EEE-II	G. VD Rao
26	18MT5A0209	GURUGUBELLI YAMINI	EEE-II	G. Yamini
27	18MT5A0210	JEERI KIRAN KUMAR	EEE-II	J. Kiran Kumar
28	18MT5A0211	KAKARLA JAGADEESWARA RAO	EEE-II	K. Jagadeeswara Rao
29	18MT5A0212	KAKARLA YAMINI	EEE-II	K. Yamini
30	18MT5A0213	KURAMAPU SUCHITHRA	EEE-II	K. Suchithra


IQAC


PROGRAMME COORDINATOR



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

Accredited by NAAC with 'A' Grade

(Approved by AICTE, New Delhi; Affiliated to JNTUK, Kakinada; G.O.Ms.No. 101, Dt. 09-07-08)

NH-5, ETCHERLA-532410, SRIKAKULAM(Dist.), ANDHRA PRADESH

Web : www.svcet.info, Email : principal_svcet@yahoo.com

Date: 07/03/2019

PROGRAM REPORT

It is to inform that “**A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING**” has been conducted **on 06/03/2019** [10am to 4pm] in the Digital hall of EEE department.

The following member is invited as a Resource person for the seminar.

NAME: Dr.P.DEVENDRA, Professor in Department of EEE, G.V.P College of Engineering for Women, Visakhapatnam.

Mr.J.Hareesh, Asst.Professor, Dept.of EEE is the coordinator of the program.

Number of Students attended: 30

The Resource person has expressed the importance and latest research findings and technological advancements in electrical engineering. That updates participants on current trends and best practices on research in Electrical engineering. All the participants felt highly impressed and motivated of the Seminar.

OBJECTIVES OF THE PROGRAM

The objective of this one-day seminar is to provide a comprehensive platform for students in the field of latest research findings and technological advancements in electrical engineering. The seminar aims to advance of electrical engineering, contribute to the professional development of participants and promote a collaborative and innovative research environment.

OUTCOMES OF THE PROGRAM

All the students who have participated in “**A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL ENGINEERING**” have felt free and realized the importance and very much enthusiastic to get them absorbed in research after completion of their studies. Participants will gain in-depth knowledge of the latest research, trends, and advancements on Electrical Engineering. 30 students participated in the program.



SRI VENKATESWARA COLLEGE OF ENGINEERING & TECHNOLOGY

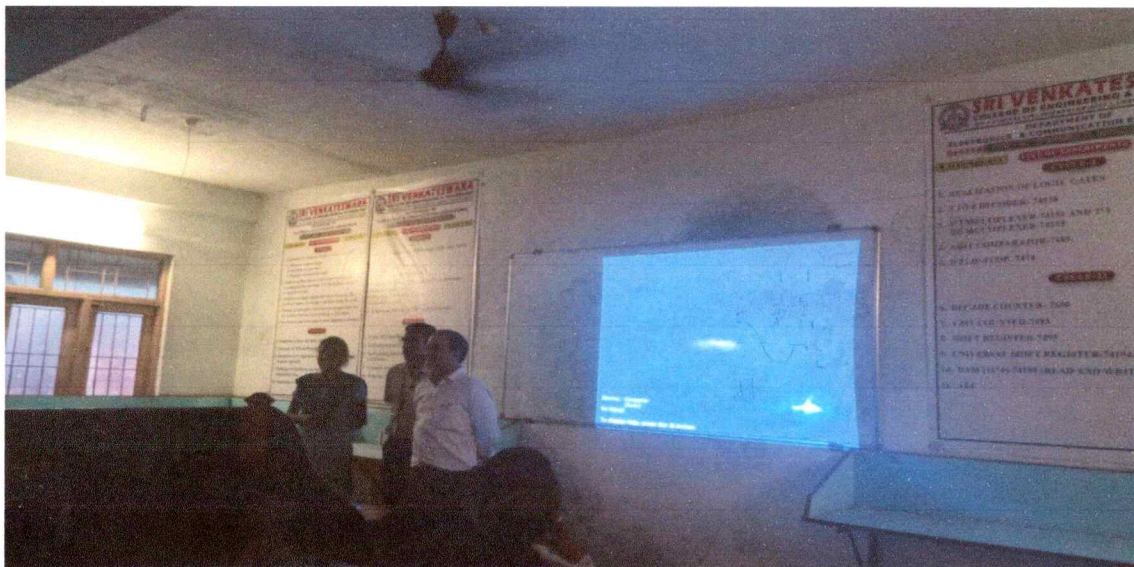
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**A ONE DAY SEMINAR ON RESEARCH AND NEW TRENDS IN ELECTRICAL
ENGINEERING**



J. Hareesh
Signature of the coordinator



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NH-5, ETCHERLA-532410, SRIKAKULAM (Dist.), ANDHRA PRADESH

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CIRCULAR

Date 03-04-2019

This is to inform to all the students of B-Tech that we are going to conduct **A One Day Seminar On Intellectual Property Rights: Changing Dimensions Of Patent Laws In India** on **10/04/2019** [10A.M to 4P.M] All the students have to report at the seminar hall sharp at 9:30A.M with the proper dress code and ID cards. Attendance is mandatory.

Resource person: Mr. V.K.Chkravarthi, Associate Professor, Department of Civil Engineering, SVCET, Etcherla, Srikakulam

All the students have to attend the seminar in the digital hall of the BS&H department at 9:30A.M












This program is aimed to: To learn, understand and analyze the Laws and Relations relating to Intellectual Property Rights in India.


Program coordinator


Principal
Principal
Sri Venkateswara College of Engineering & Technology
ETCHERLA, Srikakulam-532410 (A.P)

Submitted to: Honorable Management for Favor of Information

Copy To:

Department	IQAC	CE	EEE	ME	ECE	CSE	BS&H	MBA	OFFICE	NB	FILE
Signature											

About the Institution:

Sri Venkateswara College of Engineering & Technology ETCHERLA were established in 2008 by VIKAS EDUCATIONAL SOCIETY (1997) with an avowed objective of accelerating the growth of science and technology in this industrially backward Srikakulam district. The Institution aims at providing state-of-the-art technology to the student community to broaden their horizon for a better and prospective tomorrow in their own interest and also in interest of the society at large. We have extensive record in the field of education. The institution is endowed with highly qualified and seasoned faculty on its rolls. Sri VENKATESWARA College of Engineering & Technology is committed to "impart quality education to the youth enable them the right attitude, professional competency and inculcate ethical values".

About the Department:

Computer Science and Engineering is a profession that uses science, technology and problem solving skills will acquire knowledge of the design, implementation, and management of information systems of both hardware and Software. The department of CSE was established in 2008 with aim of importing high quality technical education in the area with an intake of 60.

CHIEF PATRON:

Dr. B. SRI RAMA MURTHY,
SECRETARY,
VIKAS EDUCATIONAL SOCIETY

PATRON:

Dr. M. GOVINDHA RAJU
Principal, SVCET

CO-ORDINATOR:

Mr. A. V. MAHESH
Asst. Professor, CSE Dept
CELL: 7893450469
EMAIL: maheshav2057@gmail.com

RESOURCE PERSON:

Mr. V. K. Chakravarthi
Associate Professor
Department of Civil Engineering
SVCET, Etcherla, Srikakulam

"A ONE DAY SEMINAR ON INTELLECTUAL PROPERTY RIGHTS: CHANGING DIMENSIONS OF PATENT LAWS IN INDIA"

10-04-2019



Organized by

**DEPARTMENT OF COMPUTER SCIENCE AND
ENGINEERING**

**SRI VENKATESWARA COLLEGE OF
ENGINEERING AND TECHNOLOGY**

**(Under the Management of Vikas Educational
Society, Srikakulam)**

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JNTUK, Kakinada; G.O.Ms.No.101, Dt. 09-07-08)
NH-5, ETCHERLA-532410, SRIKAKULAM (Dt)
ANDHRA PRADESH**

Website: www.svcet.info,

Email: principal_svcet@yahoo.com

Department Vision:

Our vision is to be a global leader in computer science and engineering education, research, and innovation.

Department Mission:

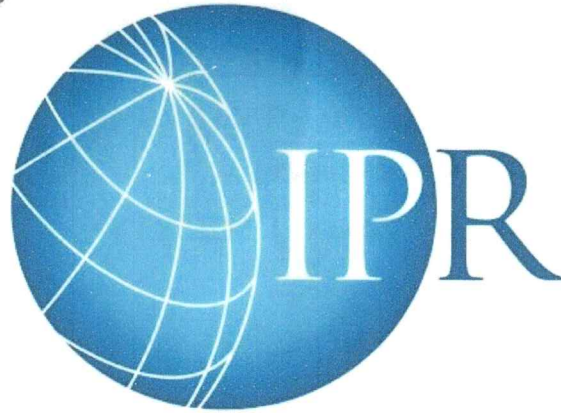
Our mission is to advance the frontiers of computer science and engineering through excellence in education, research, and innovation. We are committed to providing our students with a comprehensive and cutting-edge education that prepares them to become leaders and innovators in the rapidly evolving field of computing.

Objective:

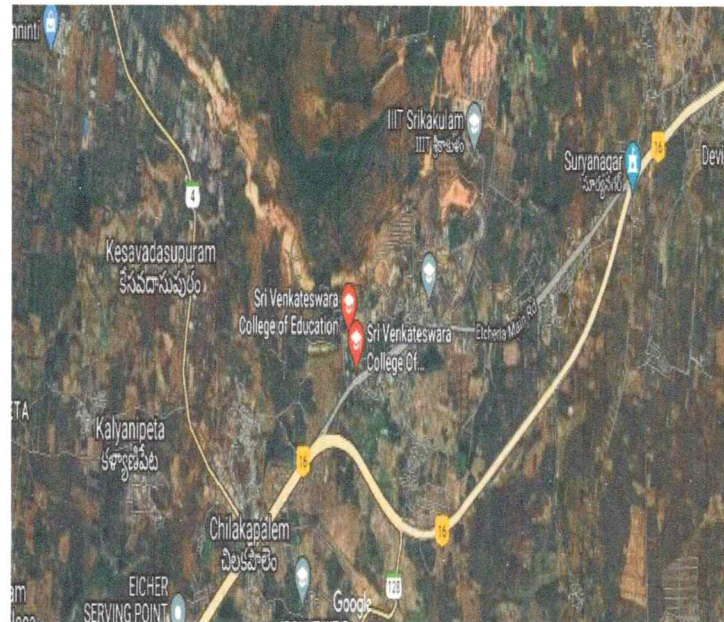
The seminar aims to contribute towards building a more conducive environment for innovation, creativity, and knowledge-driven growth in India.

Expected Outcomes:

In conclusion, the one-day seminar on Intellectual Property Rights: Changing Dimensions of Patent Laws in India served as a platform for insightful discussions, knowledge dissemination, and networking among stakeholders from academia, industry, and government. The event underscored the importance of continuous dialogue and collaboration in shaping robust intellectual property regimes that foster innovation, protect rights, and drive sustainable economic development.



Route Map:



Sri Venkateswara College of Engineering and Technology
(Under the Management of Vikas Educational Society, Srikakulam)

REGISTRATION FORM

**“A ONE DAY SEMINAR ON
INTELLECTUAL PROPERTY RIGHTS:
CHANGING DIMENSIONS OF PATENT
LAWS IN INDIA”**

10-04-2019

Participant's name :
Gender: Male/Female :
Year & Sem :
Branch :
Organization :
Address for Communication :

Mobile Number :
Email ID :

Date: **Signature of the Participant**

Signature of the HOD



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Web : www.svcet.info, Email : principal_svcet@yahoo.com

A ONE DAY SEMINAR ON INTELLECTUAL PROPERTY RIGHTS: CHANGING DIMENSIONS OF PATENT LAWS IN INDIA

List of Enrolled Students with Signature

Date: 10-04-2019

S-NO	NAME OF THE PARTICIPANT	REDG-NO	BRANCH	SINGNATURE
1	AIDHAM SHANMUKHA RAO	16MT1A0301	MECH-III	A. Shanmukha Rao
2	ANUPOJU PHANEENDRA	16MT1A0302	MECH-III	A. Phaneendra
3	ANUPOJU PRASANTH KUMAR	16MT1A0303	MECH-III	A. Prasanth Kumar
4	ARIKA CHANDU	16MT1A0304	MECH-III	A. Chandu
5	BADARI HIMAKAR	16MT1A0305	MECH-III	B. Pavankumar
6	BANTUPALLI PAVANKUMAR	16MT1A0307	MECH-III	B. Pavankumar
7	BEHERA BINOD	16MT1A0308	MECH-III	B. Binod
8	BOJJA SRINIVASA RAO	16MT1A0309	MECH-III	T. Sharmika
9	BUKKURU SANTOSH KUMAR	16MT1A0310	MECH-III	V. Rupa
10	BUTU CHAKRAVARTHI	16MT1A0311	MECH-III	B. Chakravarthi
11	CHEEDI RATNAKAR	16MT1A0313	MECH-III	C. Ratnakar
12	DASARI RANJIT KUMAR	16MT1A0314	MECH-III	D. Ranjit Kumar
13	DEVENDRO PRADHANO	16MT1A0316	MECH-III	D. Pradhano
14	DUPPALA SRINIVASA RAO	16MT1A0317	MECH-III	D. Srinivasa Rao
15	GEDDAVALASA NARENDRA	16MT1A0318	MECH-III	G. Narendra
16	GEDELA CHINNA RAO	16MT1A0319	MECH-III	G. Chinna Rao
17	GEDELA MOHAN	16MT1A0320	MECH-III	G. Mohan
18	GONDU SIVA RAM	16MT1A0321	MECH-III	G. Siva Ram
19	IPPILI PRASANTHA RAO	16MT1A0322	MECH-III	I. Prasanth
20	KAKARLA DURYODHANA RAO	16MT1A0324	MECH-III	K. Durgarao
21	KEELU MOHAN BABU	16MT1A0326	MECH-III	K. Mohan Babu
22	KINJARAPU ABHISEKH	16MT1A0327	MECH-III	K. Abhisekh
23	KOKKIRI DHARMARAO	16MT1A0328	MECH-III	K. Dharmarao
24	KOLA AJIT KUMAR	16MT1A0329	MECH-III	K. Dharmarao
25	KONDAGORRI GANAPATHI RAO	16MT1A0330	MECH-III	K. Jagai Swath Rao
26	KUMMARI JAGADISWARA RAO	16MT1A0331	MECH-III	K. Jagadish Rao
27	KUNA SRINIVASA RAO	16MT1A0332	MECH-III	K. Srinivasa Rao
28	LAKKOJI SRINIVASARAO	16MT1A0333	MECH-III	L. Srinivasa Rao
29	LINGALA MANMADHA RAO	16MT1A0335	MECH-III	L. Manmadharao
30	M PRAVEEN	16MT1A0336	MECH-III	M. Praveen
31	MAMIDIPALLI SEKHAR	16MT1A0337	MECH-III	M. Sekhar
32	MARUBARIKI SAITEJA	16MT1A0338	MECH-III	M. Saiteja
33	METTA PRATAP	16MT1A0339	MECH-III	M. Pratap
34	MOHANTY YASHWANTH SAI	16MT1A0340	MECH-III	Yashwanth

35	MUDDADA HAREESH	16MT1A0342	MECH-III	M. Hareesh
36	NALLABARIKI SAMUEL	16MT1A0343	MECH-III	N. Samuel
37	NARSIPURAM HEMANTH KUMAR	16MT1A0344	MECH-III	N. Hemanth Kumar
38	NUKA JAGAN MOHANRAO	16MT1A0345	MECH-III	N. Jagannathan
39	NULAKASAVALLA SUBHASH	16MT1A0346	MECH-III	N. Subhash
40	PARASELLI RAMAKRISHNA	16MT1A0347	MECH-III	P. Ramakrishna
41	PATNANA BALAKRISHNA	16MT1A0348	MECH-III	P. Balakrishna
42	PEDADA VAMSEE KRISHNA	16MT1A0349	MECH-III	P. Vamsee Krishna
43	PILAKA RAMESH	16MT1A0350	MECH-III	P. Ramesh
44	POTNURU SAI KUMAR	16MT1A0351	MECH-III	P. Saikumar
45	PUJARI KRISHNA RAO	16MT1A0352	MECH-III	P. Krishna Rao
46	RAATNALA SANTOSH	16MT1A0353	MECH-III	R. Santosh
47	SADASIVUNI SAI SANKAR	16MT1A0354	MECH-III	S. Sai Sankar


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PROGRAM REPORT

Date 11-04-2019

It is to inform that A **One Day Seminar on Intellectual Property Rights: Changing Dimensions of Patent Laws in India** has been conducted during 10th April 2019 in the Digital hall of BS&H department.

RESOURCE PERSON:

Mr. V.K.Chkravarthi

Associate Professor,

Department of Civil Engineering,

SVCET, Etcherla, Srikakulam

CO-ORDINATOR:

Mr. A V Mahesh

Assistant Professor

CSE Dept

SVCET

In today's rapidly evolving global economy, the protection of intellectual property rights (IPR) plays a pivotal role in fostering innovation, driving economic growth, and promoting competitiveness. Among the various forms of intellectual property, patents hold a special significance as they grant inventors exclusive rights to their inventions, incentivizing investment in research and development while ensuring that society benefits from technological advancements.

Number of Students Participated: 47



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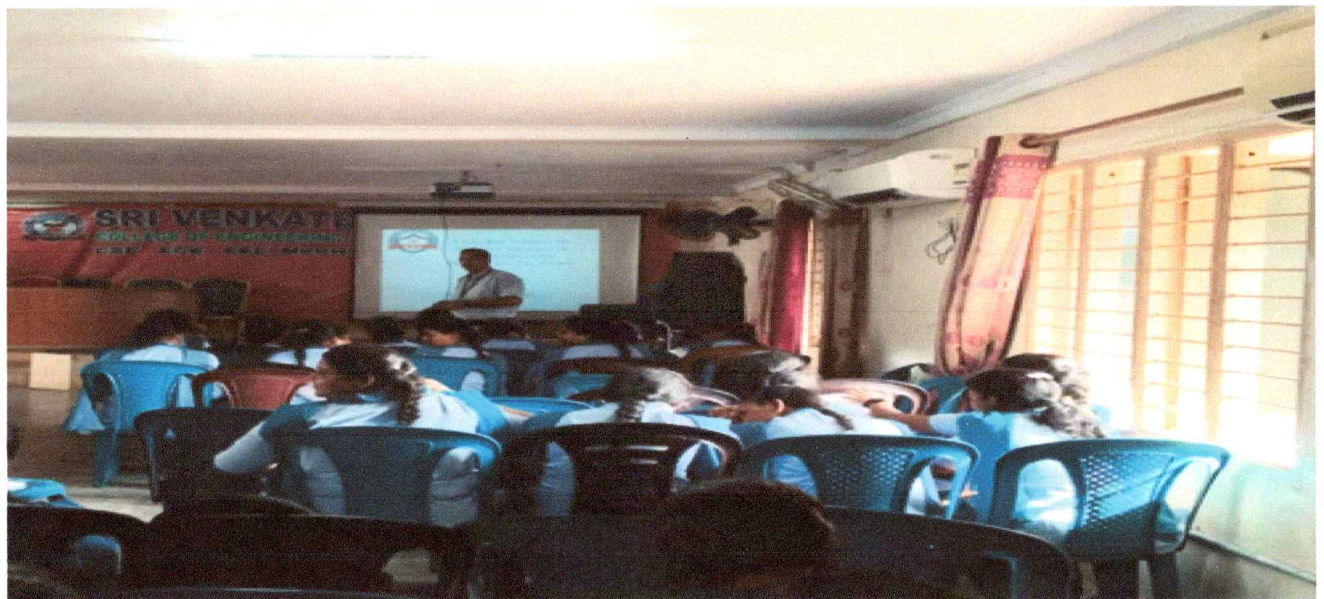
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OBJECTIVES OF THE PROGRAM

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OUTCOMES OF THE PROGRAM

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Signature of the program coordinator